RJP63K2DPP-M0

Silicon N Channel IGBT
High Speed Power Switching

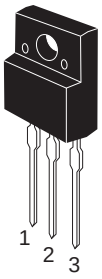
R07DS0468EJ0200
Rev.2.00
Jun 15, 2011

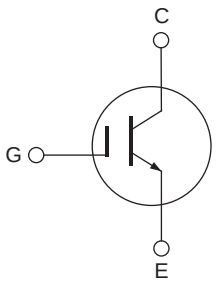
Features

- Trench gate and thin wafer technology (G6H-II series)
- Low collector to emitter saturation voltage: $V_{CE(sat)}$ = 1.9 V typ
- High speed switching: t_r = 60 ns typ, t_f = 200 ns typ.
- Low leak current: I_{CES} = 1 μA max
- Isolated package TO-220FL

Outline

RENESAS Package code: PRSS0003AF-A)
(Package name: TO-220FL)





1. Gate
2. Collector
3. Emitter

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Collector to emitter voltage	V_{CES}	630	V
Gate to emitter voltage	V_{GES}	±30	V
Collector current	I_C	35	A
Collector peak current	ic(peak) Note1	200	A
Collector dissipation	P_C Note2	25	W
Junction to case thermal impedance	θj-c	5	°C/W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%
2. Tc = 25°C

Electrical Characteristics

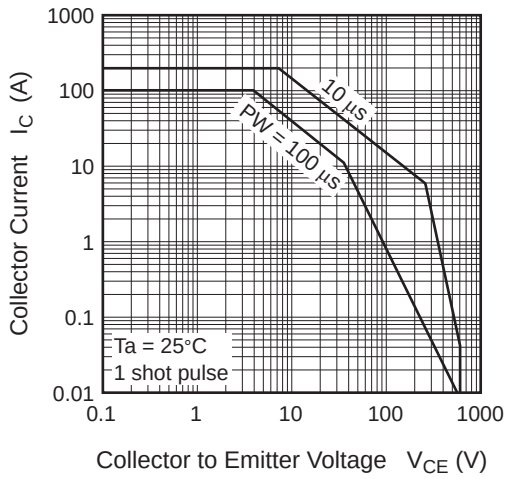
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Zero gate voltage collector current	I_{CES}	—	—	1	μA	V_{CE} = 630 V, V_{GE} = 0
Gate to emitter leak current	I_{GES}	—	—	±100	nA	V_{GE} = ± 30 V, V_{CE} = 0
Gate to emitter cutoff voltage	$V_{GE(off)}$	2.5	—	5	V	V_{CE} = 10 V, I_C = 1 mA
Collector to emitter saturation voltage	$V_{CE(sat)}$	—	1.9	2.4	V	I_C = 35 A, V_{GE} = 15 V Note3
Input capacitance	Cies	—	620	—	pF	V_{CE} = 25 V V_{GE} = 0 f = 1 MHz
Output capacitance	Coes	—	26	—	pF	
Reveres transfer capacitance	Cres	—	11	—	pF	
Total gate charge	Qg	—	20	—	nC	V_{GE} = 15 V V_{CE} = 300 V I_C = 35 A
Gate to emitter charge	Qge	—	3	—	nC	
Gate to collector charge	Qgc	—	7	—	nC	
Switching time	$t_{d(on)}$	—	0.02	—	μs	I_C = 35 A R_L = 8.5 Ω V_{GE} = 15 V R_G = 5 Ω
	t_r	—	0.06	—	μs	
	$t_{d(off)}$	—	0.05	—	μs	
	t_f	—	0.2	—	μs	

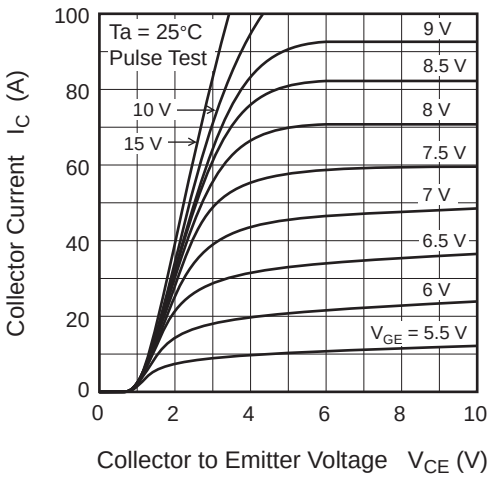
Notes: 3. Pulse test

Main Characteristics

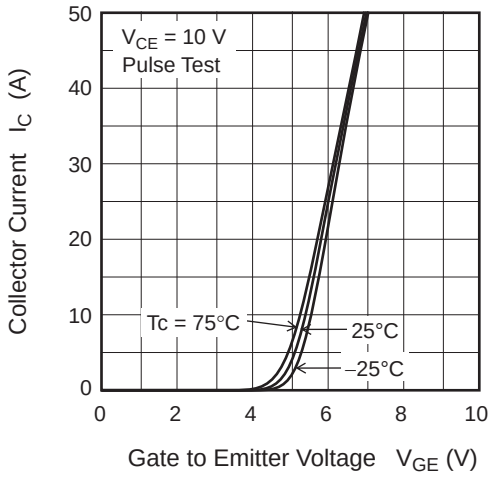
Maximum Safe Operation Area



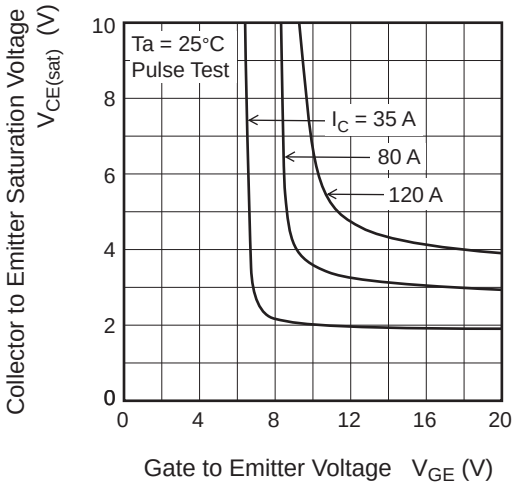
Typical Output Characteristics



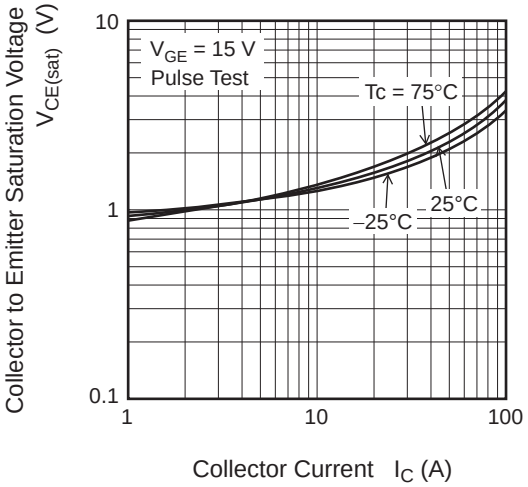
Typical Transfer Characteristics



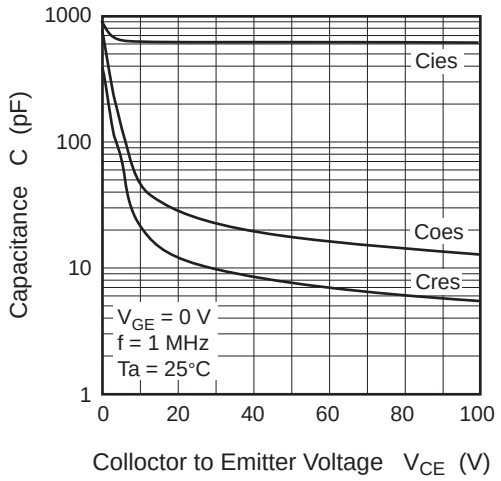
Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage (Typical)



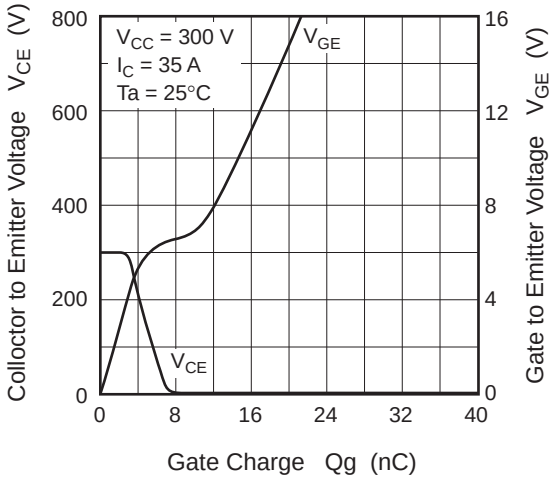
Collector to Emitter Saturation Voltage vs. Collector Current (Typical)



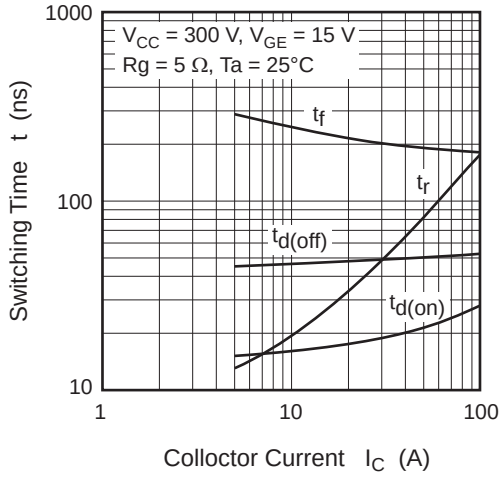
Typical Capacitance vs.
Collector to Emitter Voltage



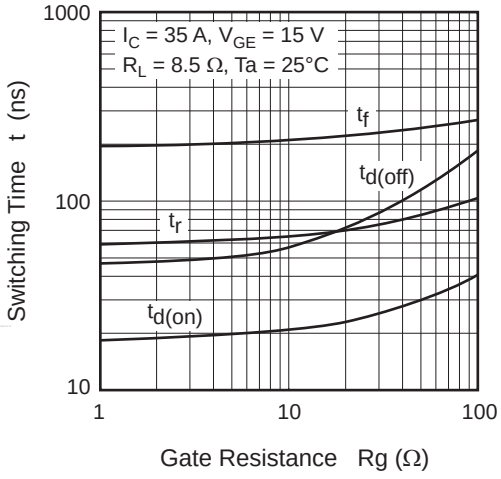
Dynamic Input Characteristics (Typical)



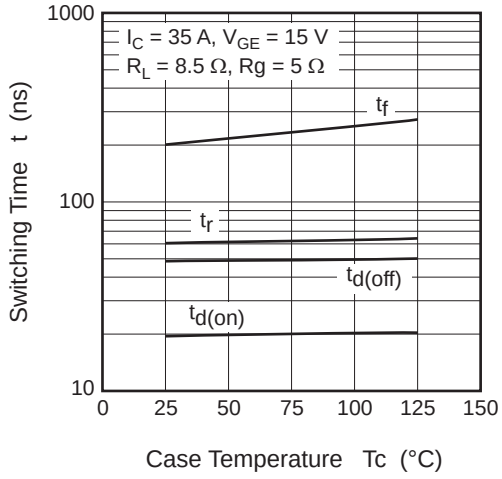
Switching Characteristics (Typical) (1)



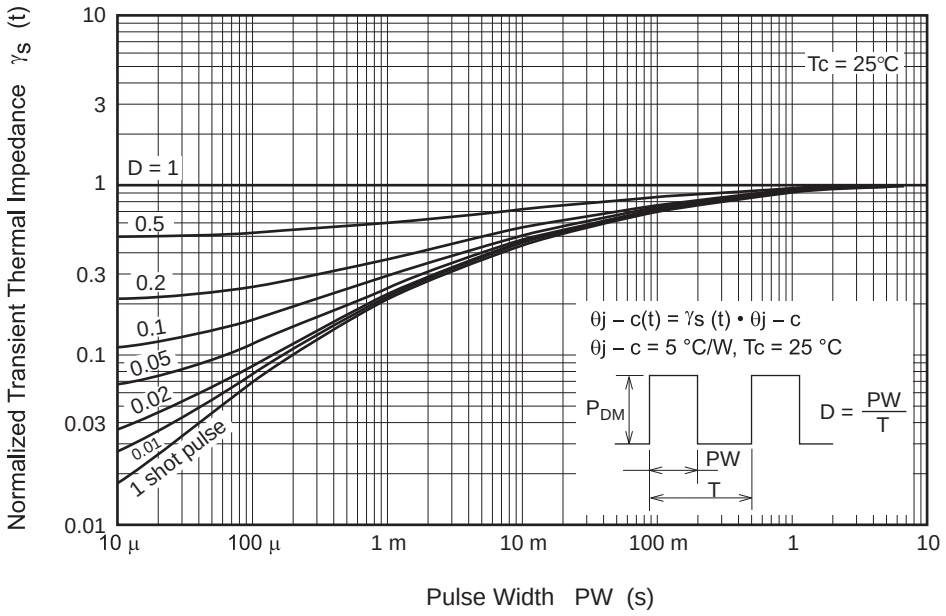
Switching Characteristics (Typical) (2)



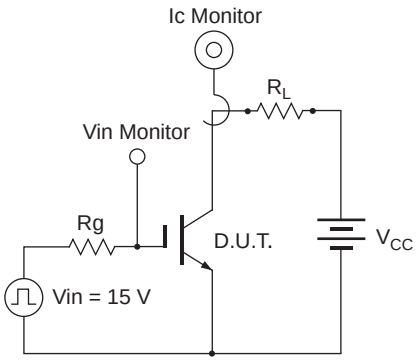
Switching Characteristics (Typical) (3)



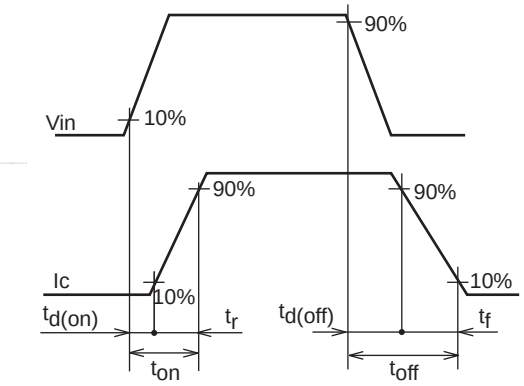
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



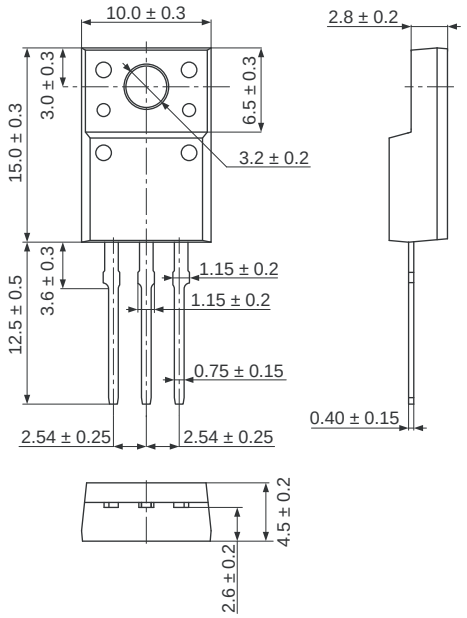
Waveform



Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
TO-220FL	—	PRSS0003AF-A	TO-220FL	1.5g

Unit: mm



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJP63K2DPP-M0-T2	600 pcs	Box (Tube)

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