

BGA6289

MMIC wideband medium power amplifier

Rev. 02 — 15 June 2009 Product data sheet

1. Product profile

1.1 General description

The BGA6289 is a silicon Monolithic Microwave Integrated Circuit (MMIC) wideband medium power amplifier with internal matching circuit in a 3-pin SOT89 plastic low thermal resistance SMD package.

The BGA6x89 series of medium power gain blocks are resistive feedback Darlington configured amplifiers. Resistive feedback provides large bandwidth with high accuracy.

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features

- Broadband 50 Ω gain block
- 17 dBm power output
- SOT89 package
- Single supply voltage needed

1.3 Applications

- Broadband medium power gain blocks
- Small signal high linearity amplifiers
- Variable gain and high output power in combination with the BGA2031
- Cellular, PCS and CDPD
- IF/RF buffer amplifier
- Wireless data SONET
- Oscillator amplifier, final PA
- Drivers for CATV amplifier

NXP founded by Philips

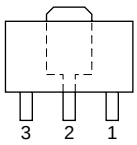
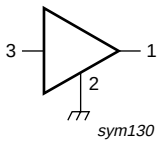
1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_D	DC device voltage	on pin 1; I_S = 88 mA	-	4.1	-	V
I_S	DC supply current	V_S = 8 V; R_{bias} = 47 Ω; T_j = 25 °C	-	88	-	mA
$\|s_{21}\|^2$	insertion power gain	f = 1.95 GHz	-	13	-	dB
NF	noise figure	f = 1.95 GHz	-	4	-	dB
P_{L1dB}	input power at 1 dB gain compression	f = 850 MHz	-	18	-	dBm
		f = 1.95 GHz	-	16	-	dBm

2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	RF_OUT/BIAS		
2	GND		
3	RF_IN		

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BGA6289	SC-62	plastic surface-mounted package; collector pad for good heat transfer; 3 leads	SOT89

4. Marking

Table 4. Marking codes

Type number	Marking code
BGA6289	3A

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5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_D	DC device voltage	on pin 1; RF input AC coupled		-	6.0	V
I_S	DC supply current			-	150	mA
P_{tot}	total power dissipation	$T_{sp} \leq 70$ °C	[1]	-	800	mW
T_{stg}	storage temperature			–65	+150	°C
T_j	junction temperature			-	150	°C
P_D	drive power			-	15	dBm

[1] T_{sp} is the temperature at the solder point of ground lead, pin 2.

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Typ	Unit
$R_{th(j-sp)}$	thermal resistance from junction to solder point	$T_{sp} \leq 70$ °C	[1]	100	K/W

[1] T_{sp} is the temperature at the solder point of ground lead, pin 2.

7. Characteristics

Table 7. Static characteristics

V_S = 8 V; T_j = 25 °C; R_{bias} = 47 Ω[1]

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_D	DC device voltage	on pin 1; I_S = 88 mA	-	4.1	-	V
I_S	DC supply current		79	88	96	mA

[1] V_S = DC operating supply voltage applied to R_{bias}; see Figure 10

Table 8. Characteristics

V_S = 8 V; I_S = 88 mA; T_{amb} = 25 °C; R_{bias} = 47 Ω; $IP3_{(out)}$ tone spacing = 1 MHz; P_L = 0 dBm per tone; $Z_L = Z_S$ = 50 Ω; unless otherwise specified; see Figure 10.

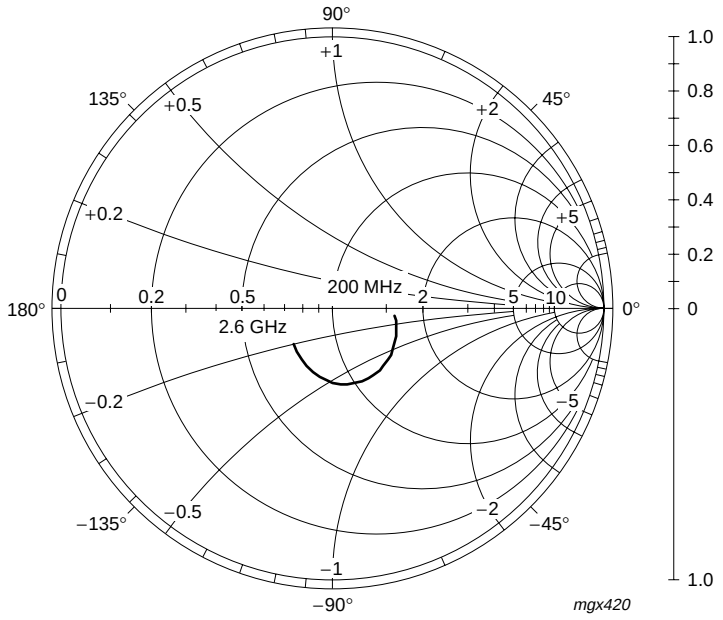
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$\|s_{21}\|^2$	insertion power gain	f = 850 MHz	-	15	-	dB
		f = 1.95 GHz	-	13	-	dB
		f = 2.5 GHz	-	12	-	dB
R_{LIN}	input return loss	f = 850 MHz	-	11	-	dB
		f = 1.95 GHz	-	11	-	dB
		f = 2.5 GHz	-	14	-	dB
R_{LOUT}	output return loss	f = 850 MHz	-	11	-	dB
		f = 1.95 GHz	-	14	-	dB
		f = 2.5 GHz	-	14	-	dB

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Table 8. Characteristics *...continued*

V_S = 8 V; I_S = 88 mA; T_{amb} = 25 °C; R_{bias} = 47 Ω; $IP3_{(out)}$ tone spacing = 1 MHz; P_L = 0 dBm per tone; Z_L = Z_S = 50 Ω; unless otherwise specified; see Figure 10.

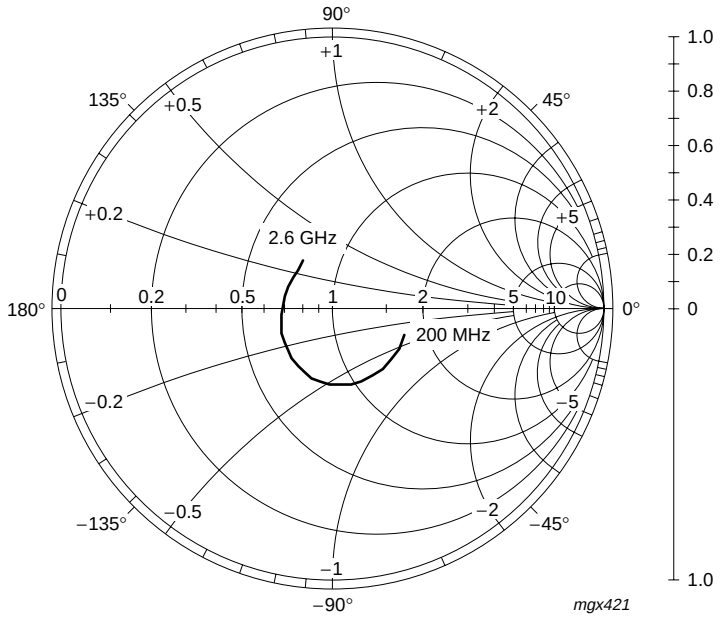
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
NF	noise figure	f = 850 MHz	-	3.5	-	dB
		f = 1.95 GHz	-	3.7	-	dB
		f = 2.5 GHz	-	3.8	-	dB
K	stability factor	f = 850 MHz	-	1.3	-	
		f = 2.5 GHz	-	1.6	-	
P_{L1dB}	output power at 1 dB gain compression	f = 850 MHz	-	17	-	dBm
		f = 1.95 GHz	-	15	-	dBm
$IP3_{(in)}$	input intercept point	f = 850 MHz	-	17	-	dBm
		f = 2.5 GHz	-	14	-	dBm
$IP3_{(out)}$	output intercept point	f = 850 MHz	-	31	-	dBm
		f = 2.5 GHz	-	25	-	dBm



I_S = 88 mA; V_S = 8 V; P_D = −30 dBm; Z_O = 50 Ω.

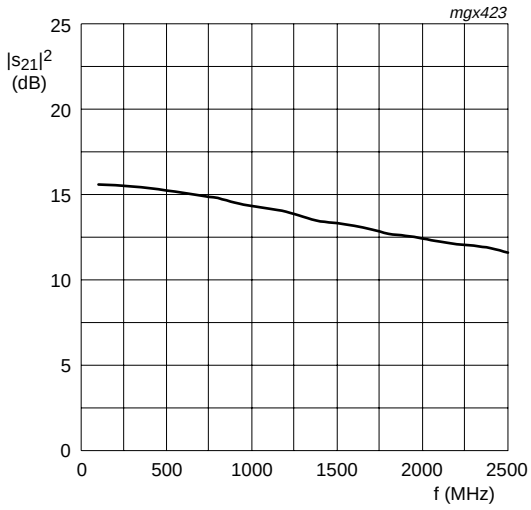
Fig 1. Input reflection coefficient (s_{11}); typical values

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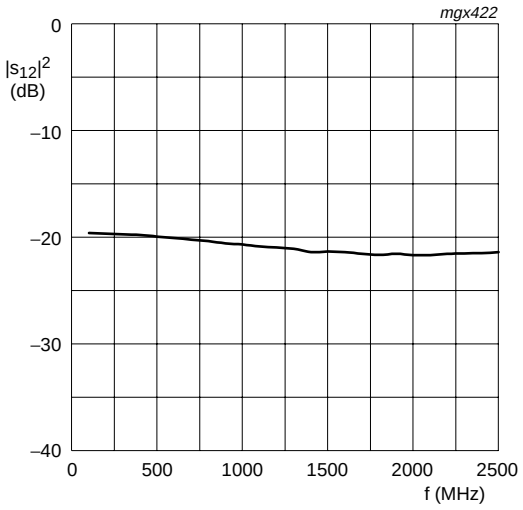
I_S = 88 mA; V_S = 8 V; P_D = −30 dBm; Z_O = 50 Ω.

Fig 2. Output reflection coefficient (s_{22}); typical values



I_S = 88 mA; V_S = 8 V; P_D = −30 dBm; Z_O = 50 Ω.

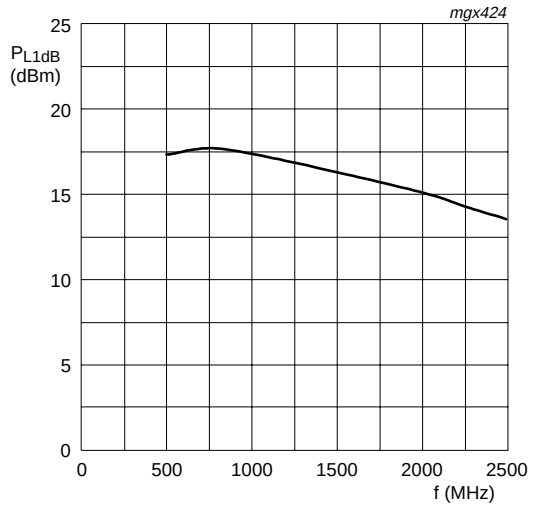
Fig 3. Insertion gain ($|s_{21}|^2$) as a function of frequency; typical values



I_S = 88 mA; V_S = 8 V; P_D = −30 dBm; Z_O = 50 Ω.

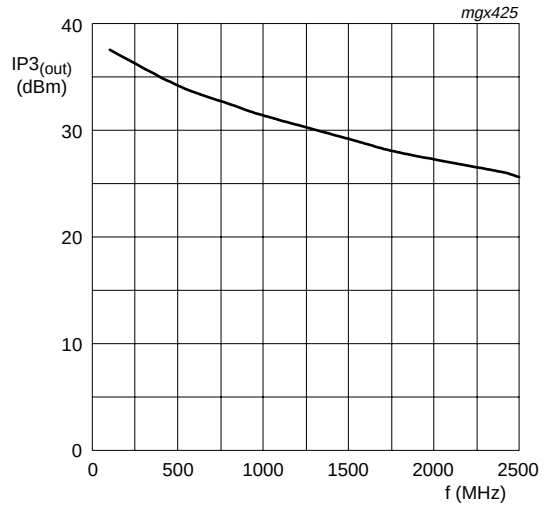
Fig 4. Isolation ($|s_{12}|^2$) as a function of frequency; typical values

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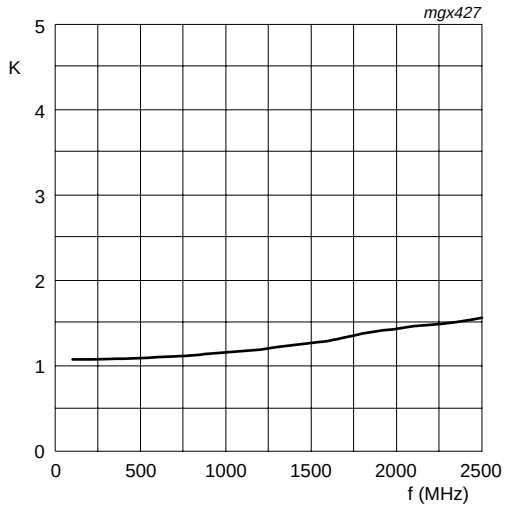
I_S = 88 mA; V_S = 8 V; Z_O = 50 Ω.

Fig 5. Load power as a function of frequency; typical values



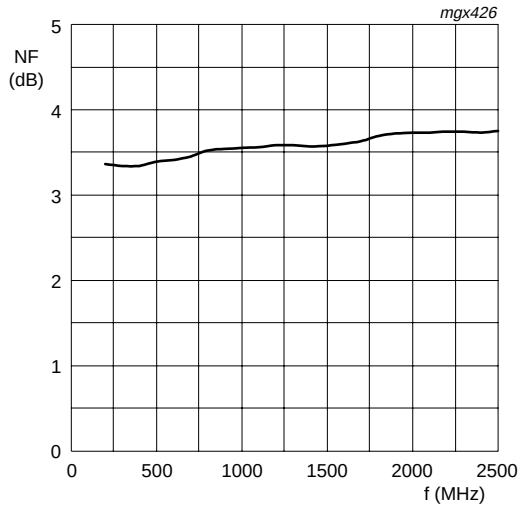
I_S = 88 mA; V_S = 8 V; P_L = 0 dBm; Z_O = 50 Ω.

Fig 6. Output intercept point as a function of frequency; typical values



I_S = 88 mA; V_S = 8 V; Z_O = 50 Ω.

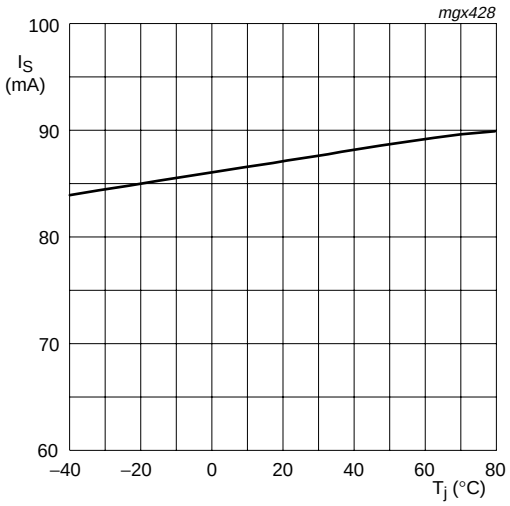
Fig 7. Stability factor as a function of frequency; typical values



I_S = 88 mA; V_S = 8 V; Z_O = 50 Ω.

Fig 8. Noise figure as a function of frequency; typical values

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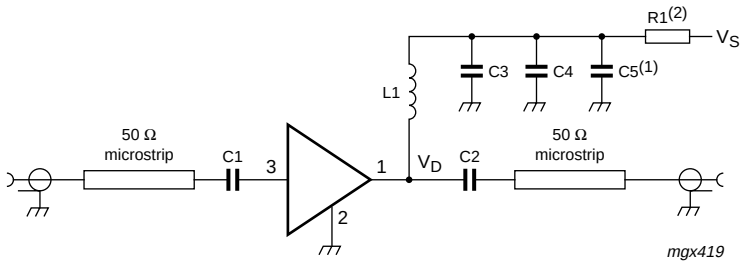


V_S = 8 V; R_{bias} = 47 Ω.

Fig 9. Supply current as a function of operating junction temperature; typical values

8. Application information

Figure 10 shows a typical application circuit for the BGA6289 MMIC. The device is internally matched to 50 Ω, and therefore does not require any external matching. The value of the input and output DC blocking capacitors C1 and C2 depends on the operating frequency; see Table 9. Capacitors C1 and C2 are used in conjunction with L1 and C3 to fine tune the input and output impedance. Capacitor C4 is a supply decoupling capacitor. A 1 µF capacitor (C5) can be added for optimum supply decoupling. The external components should be placed as close as possible to the MMIC. When using via holes, use multiple via holes per pin in order to limit ground path induction. Resistor R1 is a bias resistor providing DC current stability with temperature.



(1) Optional capacitor for optimum supply decoupling.

(2) R1 values at operating supply voltage:

V_S = 6 V; R1 = 22 Ω.

V_S = 8 V; R1 = 47 Ω.

V_S = 12 V; R1 = 91 Ω.

Fig 10. Typical application circuit

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Table 9. List of components

See Figure 10 for circuit.

Component	Description	Package	Value at operating frequency				
			500 MHz	800 MHz	1950 MHz	2400 MHz	3500 MHz
C1, C2	multilayer ceramic chip capacitor	0603	220 pF	100 pF	68 pF	56 pF	39 pF
C3	multilayer ceramic chip capacitor	0603	100 pF	68 pF	22 pF	22 pF	15 pF
C4	multilayer ceramic chip capacitor	0603	1 nF	1 nF	1 nF	1 nF	1 nF
C5[1]	electrolytic or tantalum capacitor	0603	1 µF	1 µF	1 µF	1 µF	1 µF
L1	SMD inductor	0603	68 nH	33 nH	22 nH	18 nH	15 nH
R1	SMD resistor 0.5 W; V_S = 8 V	-	47 Ω	47 Ω	47 Ω	47 Ω	47 Ω

[1] Optional.

Table 10. Scattering parameters

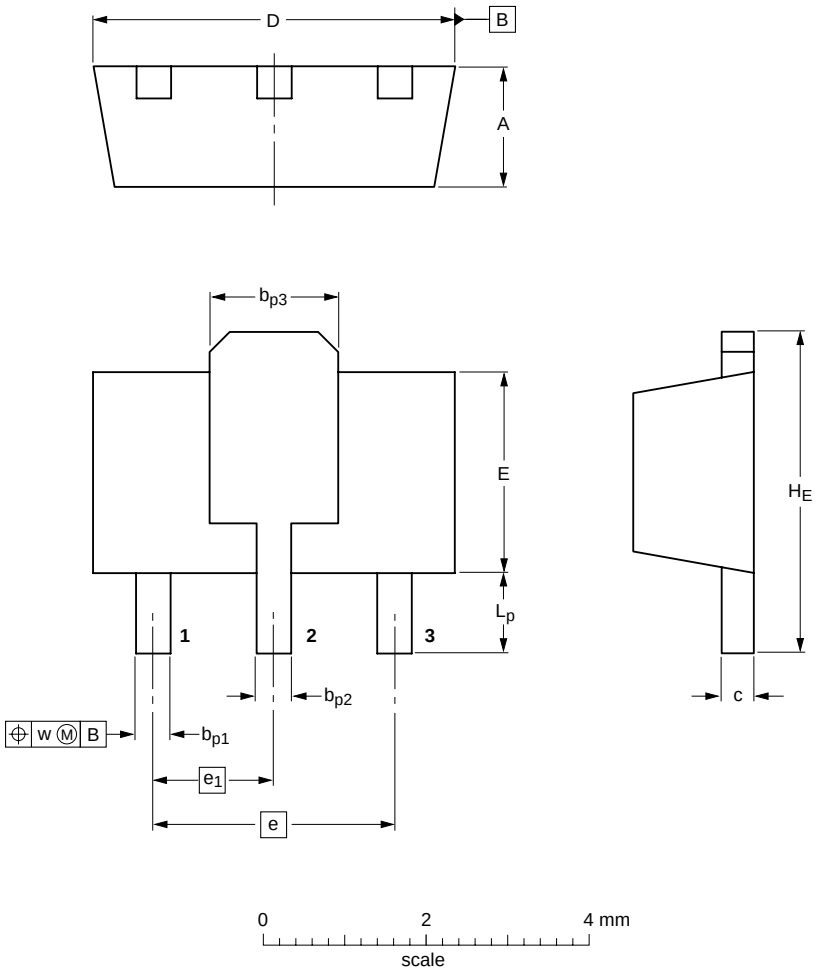
I_S = 88 mA; V_S = 8 V; P_D = −30 dBm; Z_O = 50 Ω; T_{amb} = 25 °C.

f (MHz)	s_{11}		s_{21}		s_{12}		s_{22}		K
	Magnitude (ratio)	Angle (degree)	Magnitude (ratio)	Angle (degree)	Magnitude (ratio)	Angle (degree)	Magnitude (ratio)	Angle (degree)	
800	0.327	−33.05	5.49	134.89	0.10	−11.30	0.29	−76.80	1.2
1000	0.28	−42.87	5.20	124.72	0.09	−12.71	0.28	−92.51	1.2
1200	0.29	−52.85	5.00	115.06	0.09	−13.51	0.27	−107.2	1.3
1400	0.29	−62.55	4.69	105.73	0.09	−13.66	0.25	−121.6	1.4
1600	0.29	−73.03	4.55	97.33	0.09	−13.18	0.23	−136.8	1.4
1800	0.28	−83.21	4.31	88.55	0.08	−12.17	0.21	−153.4	1.5
2000	0.26	−94.25	4.18	80.63	0.08	−12.11	0.19	−172.3	1.5
2200	0.24	−106.7	4.02	72.01	0.08	−10.45	0.18	166.36	1.5
2400	0.22	−120.4	3.91	63.83	0.08	−10.70	0.18	144.2	1.6
2600	0.19	−137.7	3.71	55.62	0.09	−10.65	0.20	122.13	1.2

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9. Package outline

Plastic surface-mounted package; collector pad for good heat transfer; 3 leads **SOT89**



DIMENSIONS (mm are the original dimensions)

UNIT	A	b_{p1}	b_{p2}	b_{p3}	c	D	E	e	e_1	H_E	L_p	w
mm	1.6 1.4	0.48 0.35	0.53 0.40	1.8 1.4	0.44 0.23	4.6 4.4	2.6 2.4	3.0	1.5	4.25 3.75	1.2 0.8	0.13

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT89		TO-243	SC-62			~~06-03-16~~ 06-08-29

Fig 11. Package outline SOT89 (SC-62)

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10. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDPD	Cellular Digital Packet Data
IF	Intermediate Frequency
PCS	Personal Communication Service
SMD	Surface Mount Device
SONET	Synchronous Optical NETwork

11. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BGA6289_2	20090615	Product data sheet	-	BGA6289_1
Modifications	• The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. • Legal texts have been adapted to the new company name where appropriate. • Changed I_S from 84 mA to 88 mA throughout. • Table 1: changed symbol V_S to V_D. • Table 5: changed symbol V_S to V_D and added “on pin 1;” to Conditions. • Table 7: added row for V_D DC device voltage. • Section 8: added sentence. • Figure 10: figure notes modified. • Table 9: changed V_S = 9 V to 8 V. • Table 9: added 47 Ω to all value columns for resistor R1 and amended values of C3 and C4.			
BGA6289_1	20030918	Product data sheet	-	-

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12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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