2.5MHz, FAST TRANSIENT 2A STEP-DOWN CONVERTER

Description

The PAM2316 is a 2A step-down sync converter. The 2.5MHz switching frequency enables the use of small external components. The ultra-small 2mm x 2mm footprint and high efficiency make the PAM2316 an ideal choice for portable applications.

The PAM2316 delivers A maximum output current while consuming only 55μA no load quiescent current. Low $R_{DS(ON)}$ integrated MOSFETs and 100 duty cycle operation make the PAM2316 the ideal choice for high output voltage, high current applications which require a low dropout threshold.

The PAM2316 provides excellent transient response and output accuracy across the operating range.

The PAM2316 maintains high efficiency throughout the load range. The PAM2316 automatically optimizes efficiency during light load mode (PSM) and maintains constant frequency and low output ripple during PWM mode.

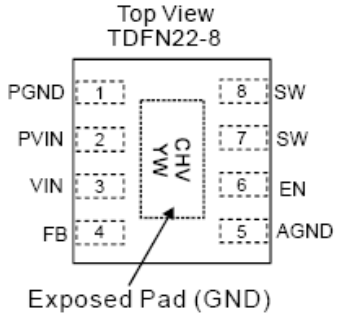
Over-temperature and short circuit protection safeguard the PAM2316 and system components from damage.

The PAM2316 is available in a Pb-free, ultrasmall, low profile, 8-pin 2mmx2mm TDFN package. The product is rated over a temperature range of -40°C to +85°C.

Features

- 2A Maximum Output Current
- Tiny 1.0μH Chip Inductor
- Excellent Transient Response
- Input Voltage: 2.7V to 5.5V
- Ultra-small, Low Profile 8-pin 2mmx2mm TDFN Package
- Fixed or Adjustable Output Voltage
  - Adjustable Output Voltage: 1.0V to 3.3V
- High Efficiency with 2.5MHz Switching Frequency
- 55μA No Load Quiescent Current
- 100% Duty Cycle Low-Dropout Operation
- Internal Soft Start
- Over-Temperature and Current Limit Protection
- <1μA Shutdown Current
- -40°C to +85°C Temperature Range
- Pb-Free/Halogen Free Package
- RoHS/REACH Compliant

Pin Assignments

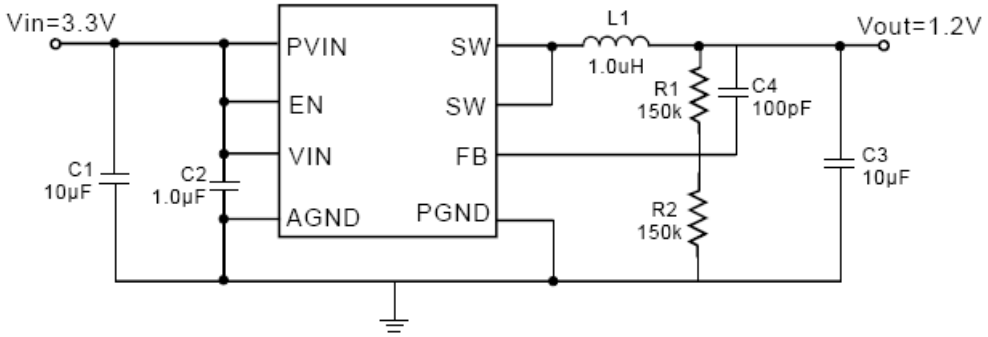


Applications

- Cellular Phone
- Digital Cameras
- Hard Disk Drives
- MP3 Players
- PDAs and Handheld Computers
- Portable Media Players
- USB Devices
- Wireless Network Cards

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Typical Applications Circuit

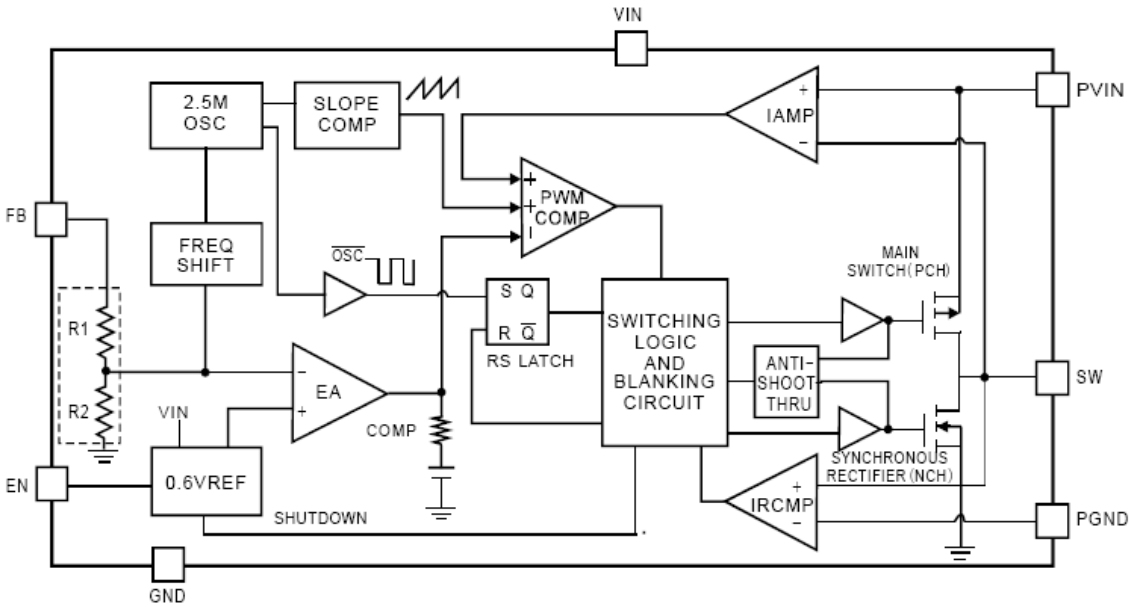


V_{OUT} = 0.6* (1+R1/R2)

Pin Descriptions

Pin Number	Pin Name	Function
1	PGND	Main power ground return pin. Connect to the output and input capacitor return.
2	PVIN	Input power supply tied to the source of the high side P-channel MOSFET.
3	VIN	Power supply; supplies power for the internal circuitry.
4	FB	Feedback input pin. Feedback voltage to internal error amplifier, the threshold voltage is 0.6V.
5	AGND	Analog ground. This pin is internally connected to the analog ground of the control circuitry.
6	EN	Enable pin. A logic low disables the converter and it consumes less than 1µA of current.When connected high, it resumes normal operation.
7, 8	SW	Switching node. Connect the inductor to this pin. It is internally connected to the drain of both high and low side MOSFETs.

Functional Block Diagram



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Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

These are stress ratings only and functional operation is not implied. Exposure to absolute maximum ratings for prolonged time periods may affect device reliability. All voltages are with respect to ground.

Parameter	Rating	Unit
Input Voltage	-0.3 to +6.5	V
EN, FB Pin Voltage	-0.3 to V_{IN}	V
SW Pin Voltage	-0.3 to (V_{IN} +0.3)	V
Junction Temperature	150	°C
Storage Temperature Range	-65 to +150	°C
Soldering Temperature	300, 5sec	°C

Recommended Operating Conditions (@T_A = +25°C, unless otherwise specified.)

Parameter	Rating	Unit
Supply Voltage	2.7 to 5.5	V
Operation Temperature Range	-40 to +85	°C
Junction Temperature Range	-40 to +125	

Thermal Information

Parameter	Package	Symbol	Max	Unit
Thermal Resistance (Junction to Case)	TDFN22-8	θ_{JC}	23.4	°C/W
Thermal Resistance (Junction to Ambient)	TDFN22-8	θ_{JA}	70	
Internal Power Dissipation	TDFN22-8	P_D	1.4	W

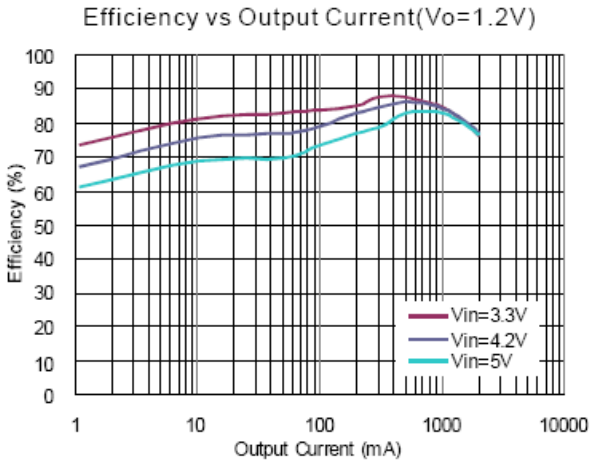
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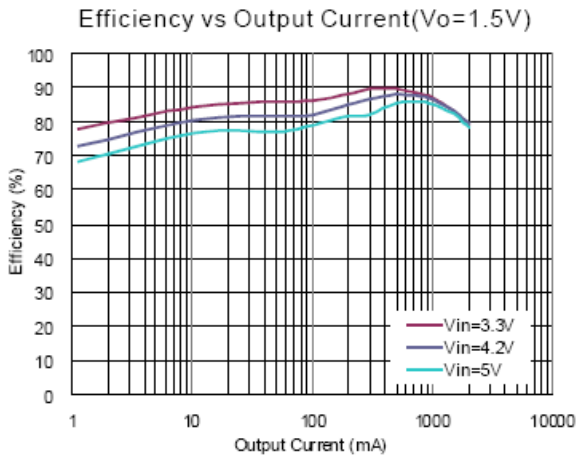
Electrical Characteristics (@T_A = +25°C, V_{IN} = 3.3V, V_{OUT} = 1.2V, C_{IN} = 10µF, C_O = 10µF, L = 1µH, unless otherwise specified.)

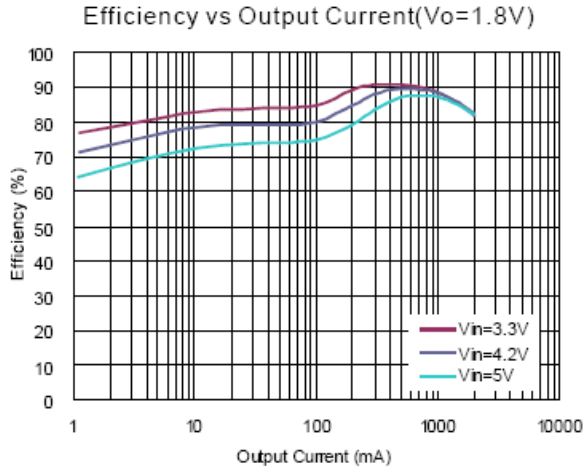
Parameter	Symbol	Test Conditions		Min	Typ	Max	Units
Input Voltage Range	V_{IN}			2.7	3.3	5.5	V
UVLO Threshold	V_{UVLO}	V_{IN} Rising			2.6	2.7	V
		Hysteresis			250		mV
		V_{IN} Falling		2			V
Output Voltage Range	V_{OUT}			1	1.2	3.3	V
Output Voltage Accuracy	V_{OUT}	I_O = 0 to 2A		-3.0		+3.0	%
Regulated Feedback Voltage	V_{FB}	No Load		0.591	0.600	0.609	V
PMOS Current Limit	I_{LIM}				3.0		A
Output Voltage Line Regulation	LNR	V_{IN} = 3.3V to 4V			0.3		%/V
Output Voltage Load Regulation	LDR	I_O = 1mA to 2A		-2		+2	%
Quiescent Current	I_Q	No Load			55	90	µA
Shutdown Current	I_{SD}	V_{EN} = 0V				1	µA
Oscillator Frequency	f_{OSC}				2.5		MHz
Drain-Source On-State Resisitance	$R_{DS(ON)}$	I_{DS} = 100mA	P MOSFET		105		mΩ
			N MOSFET		70		mΩ
SW Leakage Current	ILSW					1	µA
Start-Up Time	T_S				250		µs
PSM Threshold	I_{TH}	V_{IN} = 3.3V, V_O = 1.2V			250	450	mA
EN Threshold High	V_{EH}						V
EN Threshold Low	V_{EL}			1.4		0.4	V
EN Leakage Current	I_{EN}				±0.01		µA
Over Temperature Protection	OTP				150		°C
OTP Hysteresis	OTH				30		°C

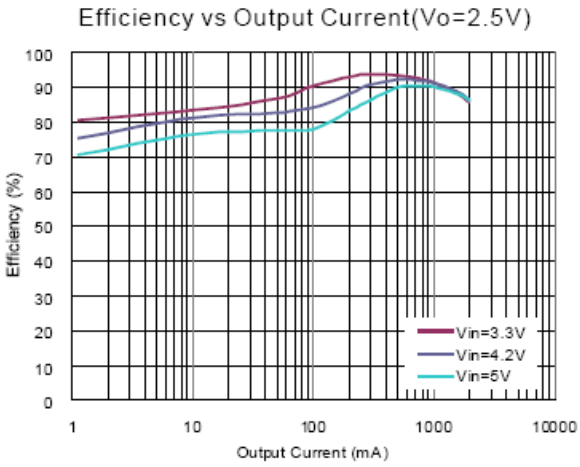
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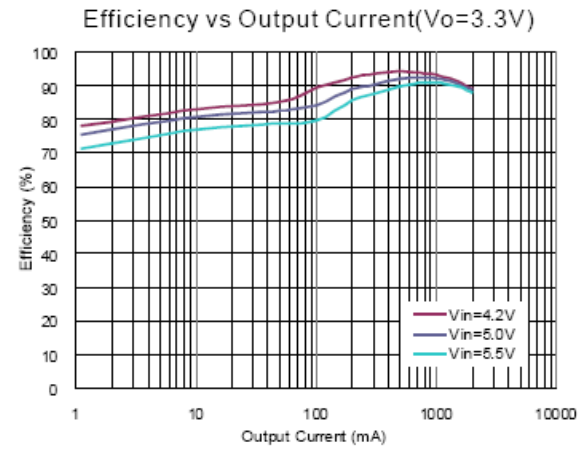
Typical Performance Characteristics (@T_A = +25°C, C_{IN} = 22*2μF, C_O = 22μF, unless otherwise specified.)

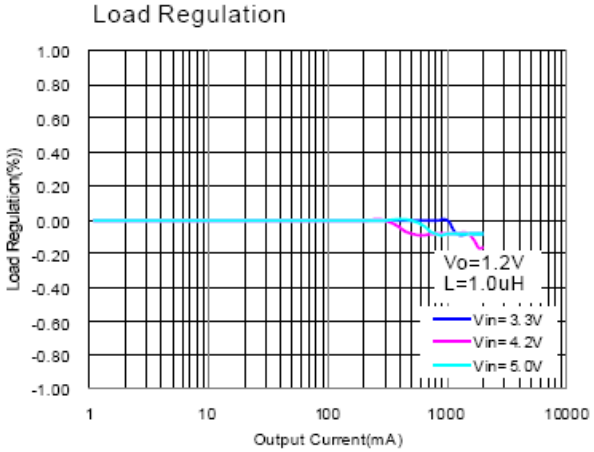








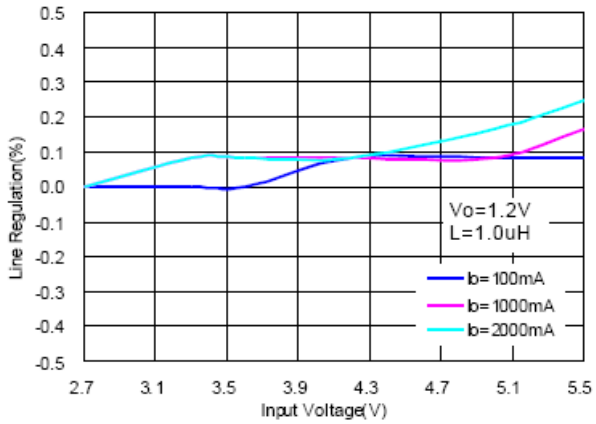




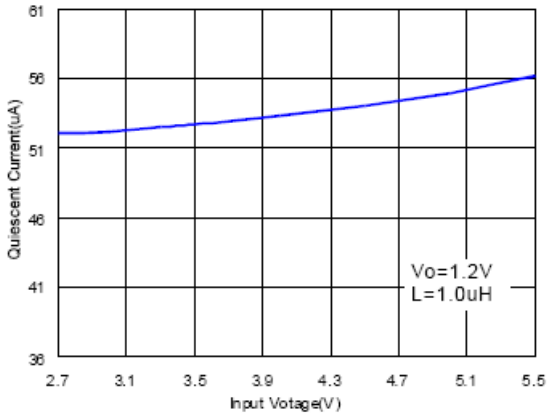
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Typical Performance Characteristics (cont.) (@T_A = +25°C, C_{IN} = 22*2μF, C_O = 22μF, unless otherwise specified.)

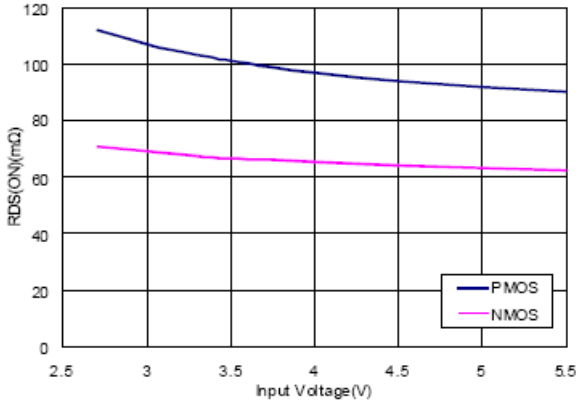
Line Regulation



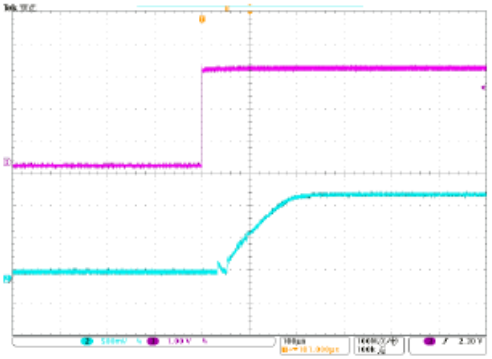
Quiescent Current vs Input Voltage



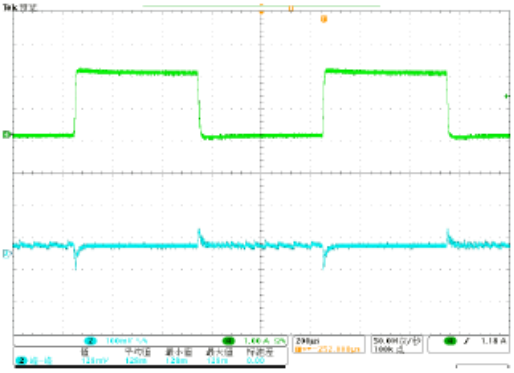
Rdson vs Input Voltage (Vo=1.2V)



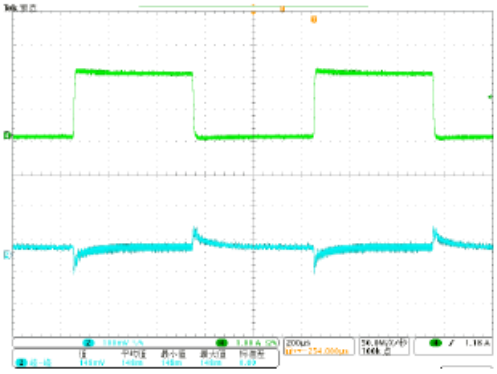
Start-up
Io=2A, Vo=1.2V, Vin=3.3V



Load Transient
Io=0~2A, Vo=1.2V, Vin=3.3V



Load Transient
Io=0~2A, Vo=3.3V, Vin=5V



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Application Information

The basic PAM2316 application circuit is shown on Page 2. External component selection is determined by the load requirement, selecting L first and then C_{IN} and C_{OUT}.

Inductor Selection

For most applications, the value of the inductor will fall in the range of 0.47μH to 2μH. Its value is chosen based on the desired ripple current. Large value inductors lower ripple current and small value inductors result in higher ripple currents. Higher V_{IN} or V_{OUT} also increases the ripple current as shown in equation 2.0A reasonable starting point for setting ripple current is ΔI_L = 800mA (40% of 2A).

$$\Delta I_L = \frac{1}{(f)(L)} V_{OUT}\left(1 - \frac{V_{OUT}}{V_{IN}}\right) \qquad \text{Equation (1)}$$

The DC current rating of the inductor should be at least equal to the maximum load current plus half the ripple current to prevent core saturation. Thus, a 2.8A rated inductor should be enough for most applications (2A + 800mA). For better efficiency, choose a low DC-resistance inductor.

C_{IN} and C_{OUT} Selection

In continuous mode, the source current of the top MOSFET is a square wave of duty cycle V_{OUT}/V_{IN}. To prevent large voltage transients, a low ESR input capacitor sized for the maximum RMS current must be used. The maximum RMS capacitor current is given by:

$$C_{IN}\,\text{required}\,I_{RMS} \cong I_{OMAX}\frac{\left[V_{OUT}\left(V_{IN} - V_{OUT}\right)\right]^{1/2}}{V_{IN}}$$

This formula has a maximum at $V_{IN} = 2V_{OUT}$, where $I_{RMS} = I_{OUT}/2$. This simple worst –case condition is commonly used for design because even significant deviations do not offer much relief. Note that the capacitor manufacturer's ripple current ratings are often based on 2000 hours of life. This makes it advisable to further derate the capacitor, or choose a capacitor rated at a higher temperature than required. Consult the manufacturer if there is any question. The selection of C_{OUT} is driven by the required effective series resistance (ESR).

Typically, once the ESR requirement for C_{OUT} has been met, the RMS current rating generally far exceeds the I_{RIPPLE} (P-P) requirement. The output ripple V_{OUT} is determined by:

$$\Delta V_{OUT} \approx \Delta I_L \left(ESR + 1/8f\,C_{OUT}\right)$$

Where f = operating frequency, C_{OUT} = output capacitance and ΔI_L = ripple current in the inductor. For a fixed output voltage, the output ripple is highest at maximum input voltage since ΔI_L increases with input voltage.

Using Ceramic Input and Output Capacitors

Higher values, lower cost ceramic capacitors are now becoming available in smaller case sizes. Their high ripple current, high voltage rating and low ESR make them ideal for switching regulator applications. Using ceramic capacitors can achieve very low output ripple and small circuit size.

When choosing the input and output ceramic capacitors, choose the X5R or X7R dielectric formulations. These dielectrics have the best temperature and voltage characteristics of all the ceramics for a given value and size.

Thermal Consideration

Thermal protection limits power dissipation in the PAM2316. When the junction temperature exceeds +150°C, the OTP (Over Temperature Protection) starts the thermal shutdown and turns the pass transistor off. The pass transistor resumes operation after the junction temperature drops below +120°C.

For continuous operation, the junction temperature should be maintained below +125°C. The power dissipation is defined as:

$$P_D = I_O{}^2\,\frac{V_O R_{DS(ON)H} + \left(V_{IN} - V_O\right)R_{DS(ON)L}}{V_{IN}} + \left(t_{SW}\,F_S\,I_O + I_Q\right)V_{IN}$$

I_Q is the step-down converter quiescent current. The term tsw is used to estimate the full load step-down converter switching losses.

For the condition where the step-down converter is in dropout at 100% duty cycle, the total device dissipation reduces to:

$$P_D = I_O{}^2\,R_{DS(ON)H} + I_Q\,V_{IN}$$

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Application Information (cont.)

Since $R_{DS(ON)}$, quiescent current, and switching losses all vary with input voltage, the total losses should be investigated over the complete input voltage range. The maximum power dissipation depends on the thermal resistance of IC package, PCB layout, the rate of surrounding airflow and temperature difference between junction and ambient. The maximum power dissipation can be calculated by the following formula:

$$P_D = \frac{T_{J(MAX)} - T_A}{\theta_{JA}}$$

Where $T_{J(MAX)}$ is the maximum allowable junction temperature +125°C. T_A is the ambient temperature and θ_{JA} is the thermal resistance from the junction to the ambient. Based on the standard JEDEC for a two layers thermal test board, the thermal resistance θ_{JA} of TDFN2X2 70°C/W, respectively. The maximum power dissipation at T_A = +25°C can be calculated by following formula:

P_D = (125°C - 25°C) /70°C/W = 1.42W

Setting the Output Voltage

The internal reference is 0.6V (Typical). The output voltage is calculated as below:

The output voltage is given by Table 1.

$$V_O = 0.6x\left(1 + \frac{R1}{R2}\right)$$

Table 1: Resistor selection for output voltage setting.

V_O	R1	R2
1.2V	150k	150k
1.5V	150k	100k
1.8V	300k	150k
2.5V	380k	120k
3.3V	680k	150k

Pulse Skipping Mode (PSM) Description

When load current decreases, the peak switch current in Power-PMOS will be lower than skip current threshold and the device will enter into Pulse Skipping Mode.

In this mode, the device has two states, working state and idle state. First, the device enters into working state control led by internal error amplifier. When the feedback voltage gets higher than internal reference voltage, the device will enter into low I_Q idle state with most of internal blocks disabled. The output voltage will be reduced by loading or leakage current. When the feedback voltage gets lower than the internal reference voltage, the convertor will start a working state again.

100% Duty Cycle Operation

As the inpput approaches the output voltage, the converter turns the P-Channel transistor continuously on. In this mode the output voltage is equal to the input voltage minus the voltage drop across the P-Channel transistor:

$$V_{OUT} = V_{IN} - I_{LOAD}(R_{DSON} + R_L)$$

where $R_{DS(ON)}$ = P-Channel Switch ON resistance, I_{LOAD} = Output Current, R_L = Inductor DC Resistance

UVLO and Soft-Start

The reference and the circuit remain reset until the V_{IN} crosses its UVLO threshold.

The PAM2316 has an internal soft-start circuit that limits the in-rush current during start-up. This prevents possible voltage drops of the input voltage and eliminates the output voltage overshoot.

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Application Information (cont.)

Short Circuit Protection

When the converter output is shorted or the device is overloaded, each high-side MOSFET current-limit event (3A typ) turns off the high-side MOSFET and turns on the low-side MOSFET. An internal counter is used to count the each current-limit event. The counter is reset after consecutive high-side MOSFETs turn on without reaching cur rent l imi t. I f the current- limit condition persists, the counter fills up. The control logic then stops both high-side and lowside MOSFETs and waits for a hiccup period, before attemping a new soft-start sequence. The counter bits is decided by V_{FB} voltage. If $V_{FB} \leq 0$ 2, the counter is 3-bit counter; if $V_{FB} > 0.2$ the counter is 6-bit counter. The typical hicuup made duty cycle is 1.7%. The hicuup mode is disable during soft-start time.

Thermal Shutdown

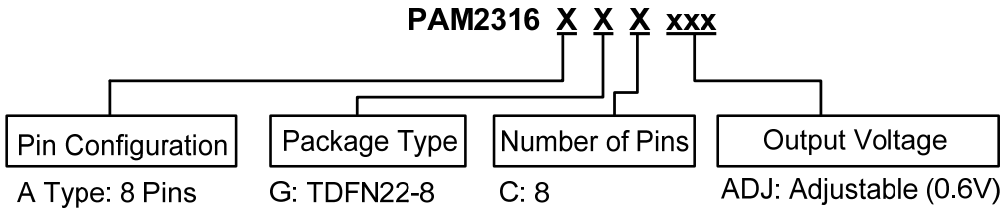
When the die temperature exceeds +150°C, a reset occurs and the reset remains until the temperature decrease to +120°C, at which time the circuit can be restarted.

PCB Layout Check List

When laying out the printed circuit board, the following checklist should be used to ensure proper operation of the PAM2316. Check the following in your layout:

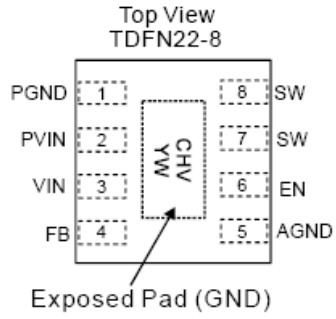
1. The input capacitor should be close to IC as close as possible.
2. Must put a small decoupling capacitor between V_{IN} Pin and AGND Pin.
3. Minimize the switching loop area to avoid excessive switching noise.
4. AGND and PGND should connect at input capacitor GND.
5. For the good thermal dissipation, PAM2316 has a heat dissipate pad in the bottom side, it should be soldered to PCB surface. For the copper area can't be large in the component side, so we can use mu ltiple vias connect to other side of the PCB.

Ordering Information



Part Number	Output Voltage	Part Type	Standard Package
PAM2316AGCADJ	CHVYW	T-DFN22-8	3000 Units/Tape & Reel

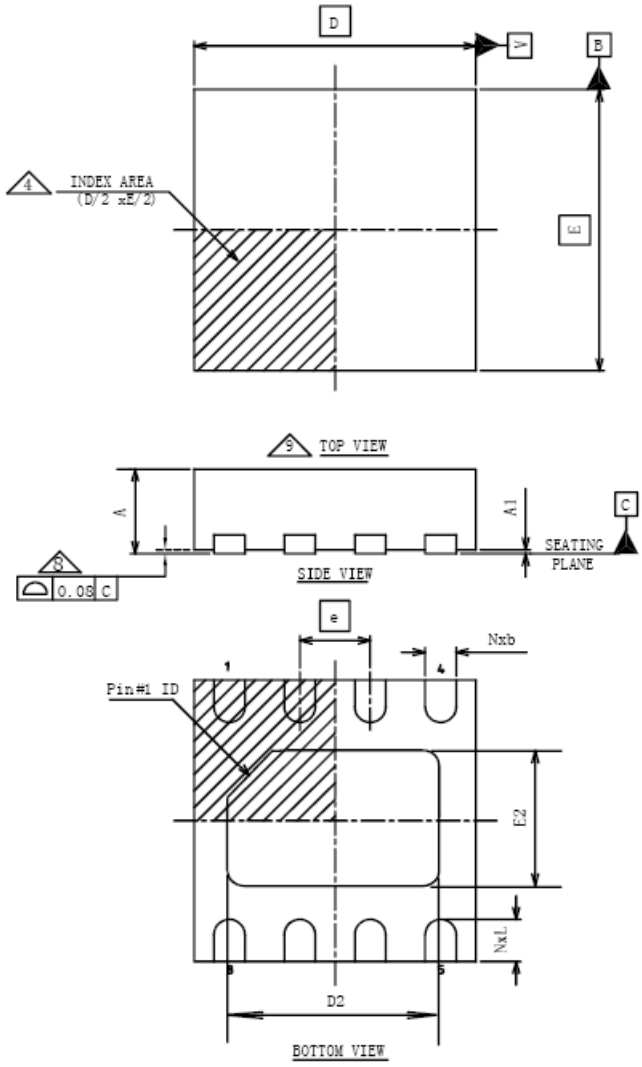
Marking Information



CH: Product Code of PAM2316
V: Output Voltage
Y: Year
W: Week

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Package Outline Dimensions (All dimensions in mm.)



	COMMON DIMENSION		
SYMBOL	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05

Summary Table			
Lead Pitch (e)	Lead Count	Body Size	Pin #1 ID
0.50	8	2X2	R0.20

D BSC		2.00
E BSC		2.00
b	MIN	0.18
	NOM	0.25
	MAX	0.30
D2	MIN	1.55
	NOM	1.60
	MAX	1.65
E2	MIN	0.85
	NOM	0.90
	MAX	0.95
L	MIN	0.20
	NOM	0.30
	MAX	0.40
N		8

Unit: Millimeters

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