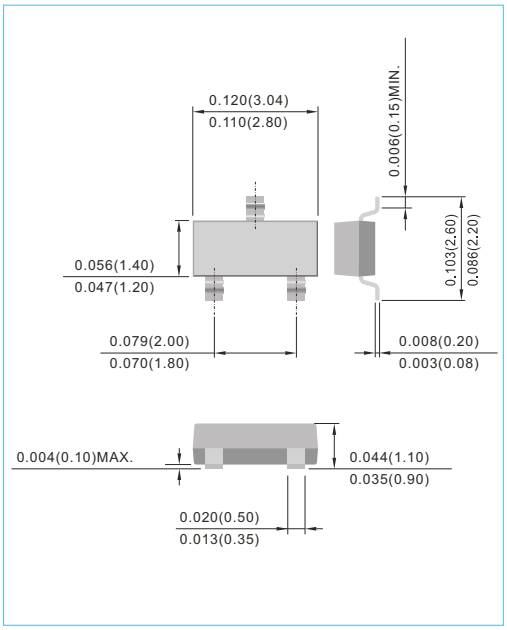
PJDLC05C-05

VOLTAGE 5.0 Volts **POWER** 400 Watts

SOT-23 Unit : inch(mm)



ULTRA LOW CAPACITANCE DUAL TRANSIET VOLTAGE SUPPRESSOR FOR HIGH SPEEDDATA LINES

This transient overvoltage suppressor is intended to protect sensitive equipment against electrostatic discharge events as well to offer a minimum insertion loss in data transmission lines in communications ports used in portable consumer, computing and networking applications. This dual transient voltage suppressor comes in a single SOT-23, offering board space reduction, where the application requires it.

This device comes with two pairs of high speed switching diodes connected in series, where both pairs are electrically isolated, offering a very low capacitance, minimizing the insertion losses in data transmission lines.

FEATURES

- Maximum capacitance @ 0 Vdc Bias of 1.0 pF between terminals 1-3 or terminals 2-3
- IEC61000-4-2 esd 15kV Air, 8kV contact compliance
- Lead free in comply with EU RoHS 2002/95/EC directives.
- Green molding compound as per IEC61249 Std. . (Halogen Free)

MECHANICAL DATA

- Case: SOT-23, plastic
- Terminals: solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams
- Marking : DEA

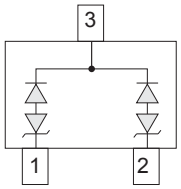


Fig.21

MAXIMUM RATINGS

Parameter	Symbol	Value	Units
Operating Junction	T_J	-55 to +150	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C

ELECTRICAL CHARACTERISTICS

PJDLC05C-05

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V_{RWM}	-	-	-	5	V
Reverse Breakdown Voltage	V_{BR}	I_T=1mA	6	-	-	V
Reverse Leakage Current	I_R	V_{RWM} = 5V, T = 25°C	-	0.8	1.2	μA
Junction Capacitance	C_J	Between pin1.2 to 3 V_R=0V,f=1MHz	-	-	1.0	pF
Peak Pulse Current	I_{PP}	t_P=8/20 μsec	-	-	17	A
Clamping Voltage	V_C	t_P=8/20 μsec @1A	-	-	9.5	V
Clamping Voltage	V_C	t_P=8/20 μsec @5A	-	-	12	V

PJDLC05C-05

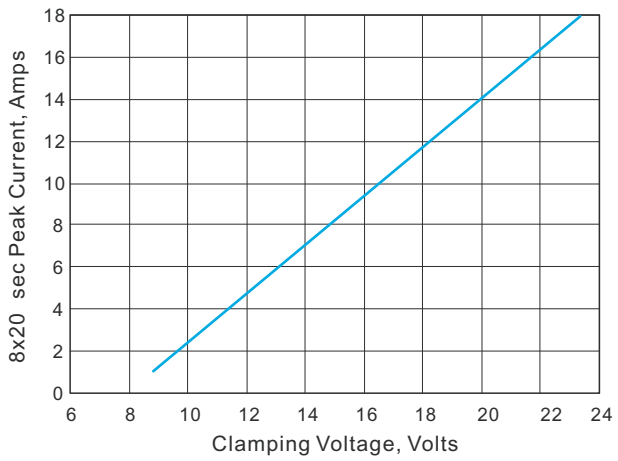


Fig 1- Clamping Voltage vs Ipp 8x20 sec

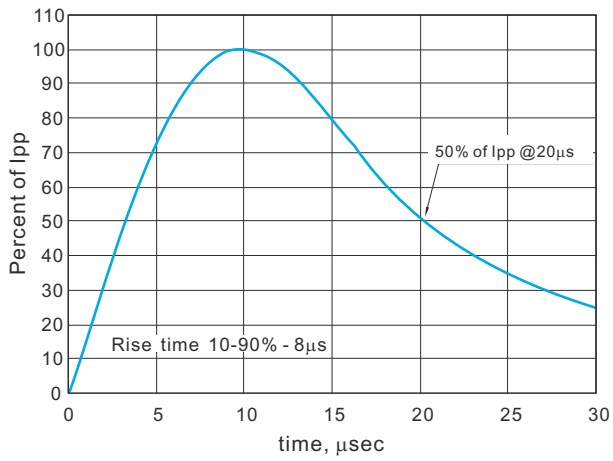


Fig 2- Pulse Waveform

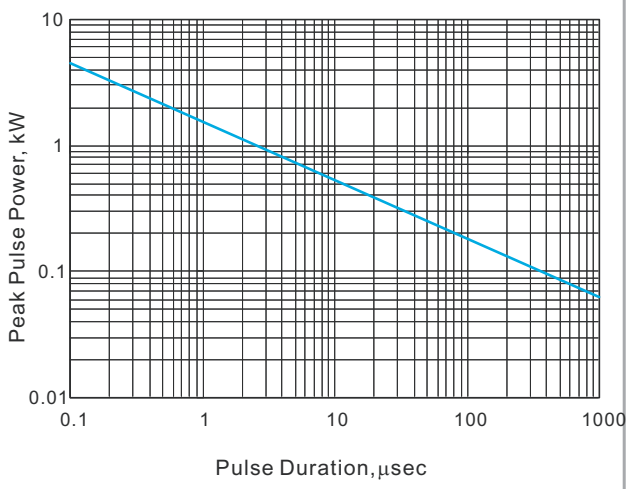


Fig 3- Non-Repetitive Peak Pulse Power vs Pulse Time

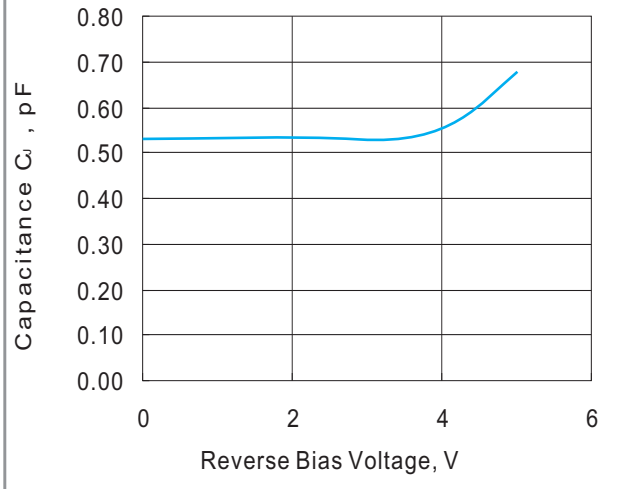
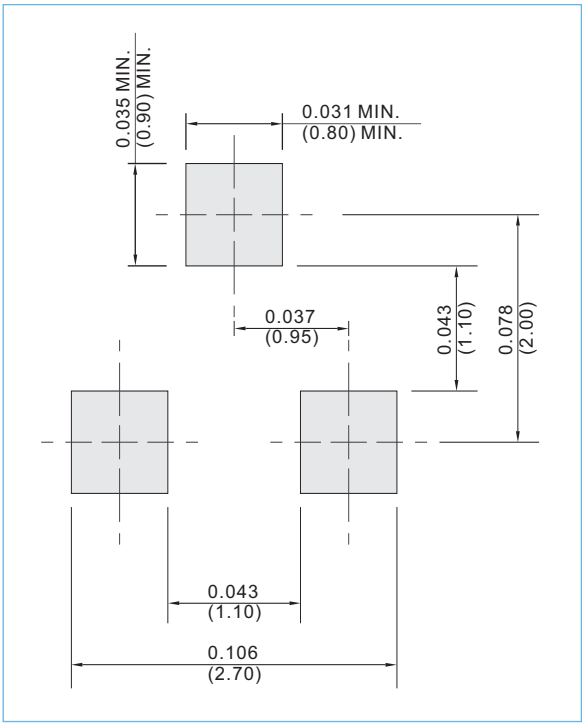


FIG.4- TYPICAL JUNCTION CAPACITANCE UNDER BIAS

PJDLC05C-05

MOUNTING PAD LAYOUT

SOT-23 Unit : inch(mm)



ORDER INFORMATION

- Packing information

  T/R - 12K per 13" plastic Reel

  T/R - 3K per 7” plastic Reel

LEGAL STATEMENT

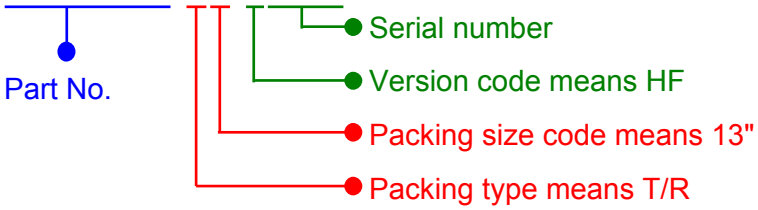
Copyright PanJit International, Inc 2012

The information presented in this document is believed to be accurate and reliable. The specifications and information herein are subject to change without notice. Pan Jit makes no warranty, representation or guarantee regarding the suitability of its produ cts for any particular purpose. Pan Jit products are not authorized for use in life support devices or systems. Pan Jit does not convey any license under its patent rights or rights of others.

PJDLC05C-05

For example :

RB500V-40_R2_00001



Part No_packing code_Version

PJDLC05C-05_R1_00001
PJDLC05C-05_R1_10001
PJDLC05C-05_R2_00001
PJDLC05C-05_R2_10001

Packing Code XX				Version Code XXXXX		
Packing type	1st Code	Packing size code	2nd Code	HF or RoHS	1st Code	2nd~5th Code
T/B	A	N/A	0	HF	0	serial number
T/R	R	7"	1	RoHS	1	serial number
B/P	B	13"	2			
T/P	T	26mm	X			
TRR	S	52mm	Y			
TRL	L	PBCU	U			
FORMING	F	PBCD	D			