Dual digital transistors

IMH21

●Features

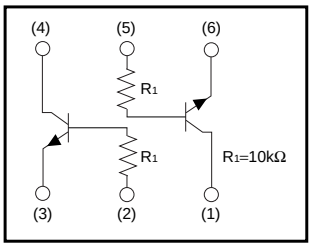
In addition to the features of regular digital transistors.

1) Low saturation voltage, typically
   $V_{CE\ (sat)}$ =40mV at I_C / I_B=50mA / 2.5mA, makes these transistors ideal for muting circuits.
2) These transistors can be used at high current levels, I_C=600mA.
3) Two DTC614T chips in a SMT package.

●Structure

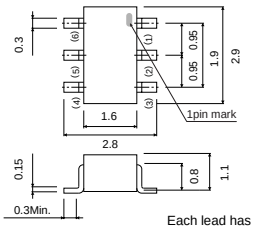
NPN digital transistor
(Built-in resistor type)

●Equivalent circuit



●External dimensions (Unit : mm)

SMT6



Each lead has same dimensions

Abbreviated symbol : H21

●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	V_{CBO}	20	V
Collector-emitter voltage	V_{CEO}	20	V
Emitter-base voltage	V_{EBO}	12	V
Collector current	I_C	600	mA
Collector power dissipation	P_C	300(TOTAL)	mW *
Junction temperature	Tj	150	°C
Storage temperature	Tstg	−55 to +150	°C

* 200mW per element must not be exceeded.

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV_{CBO}	20	–	–	V	I_C=50μA
Collector-emitter breakdown voltage	BV_{CEO}	20	–	–	V	I_C=1mA
Emitter-base breakdown voltage	BV_{EBO}	12	–	–	V	I_E=50μA
Collector cutoff current	I_{CBO}	–	–	0.5	μA	V_{CB}=20V
Emitter cutoff current	I_{EBO}	–	–	0.5	μA	V_{EB}=12V
Collector-emitter saturation voltage	$V_{CE\ (sat)}$	–	40	150	mV	I_C / I_B=50mA / 2.5mA
DC current transfer ratio	h_{FE}	820	–	2700	–	V_{CE}=5V, I_C=50mA
Input resistance	R_1	7	10	13	kΩ	–
Transition frequency	f_T	–	150	–	MHz	V_{CE}=10V, I_E= –50mA, f=100MHz *
Output "ON" resistance	R_{on}	–	0.9	–	Ω	VI=5V, R_L=1kΩ, f=1KHz

*Transition frequency of the device.

●Packaging specifications and h_{FE}

Type	Package	SMT6
	Packaging type	Taping
	Code	T110
	Basic ordering unit (pieces)	3000
IMH21		○

●Electrical characteristic curves

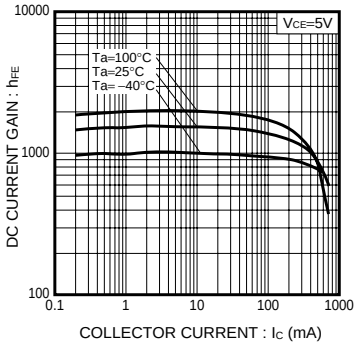


Fig.1 DC Current Gain vs. Collector Current

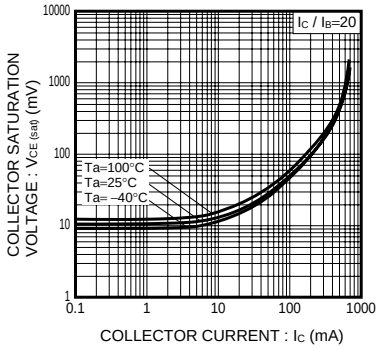


Fig.2 Collector-Emitter Saturation Voltage vs. Collector Current

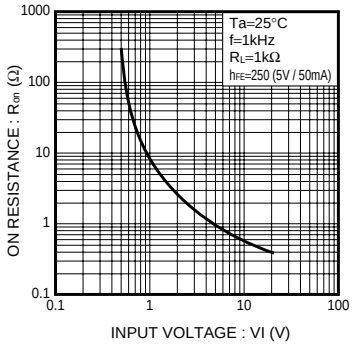


Fig.3 "ON" resistance vs. Input Voltage

●R_{on} measurement circuit

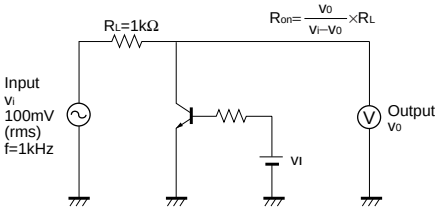


Fig.4 Output "ON" resistance (R_{on}) measurement circuit

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