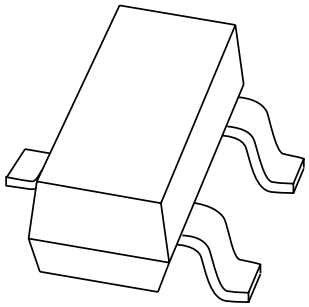
DATA SHEET



PMBT4401
NPN switching transistor

Product specification
Supersedes data of 1999 Apr 15

2004 Jan 21

Philips
Semiconductors

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NPN switching transistor PMBT4401

FEATURES

- High current (max. 600 mA)
- Low voltage (max. 40 V).

APPLICATIONS

- Industrial and consumer switching applications.

DESCRIPTION

NPN switching transistor in a SOT23 plastic package.
PNP complement: PMBT4403.

MARKING

TYPE NUMBER	MARKING CODE(1)
PMBT4401	*2X

Note

1. * = p : Made in Hong Kong.
   * = t : Made in Malaysia.
   * = W : Made in China.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector

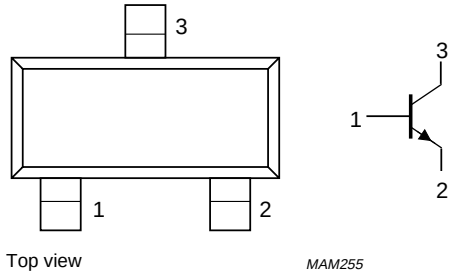


Fig.1 Simplified outline (SOT23) and symbol.

ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
PMBT4401	–	plastic surface mounted package; 3 leads	SOT23

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	60	V
V_{CEO}	collector-emitter voltage	open base	–	40	V
V_{EBO}	emitter-base voltage	open collector	–	6	V
I_C	collector current (DC)		–	600	mA
I_{CM}	peak collector current		–	800	mA
I_{BM}	peak base current		–	200	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C; note 1	–	250	mW
T_{stg}	storage temperature		–65	+150	°C
T_j	junction temperature		–	150	°C
T_{amb}	operating ambient temperature		–65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ (j\text{-}a)}$	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I_{CBO}	collector-base cut-off current	I_E = 0; V_{CB} = 60 V	–	50	nA
I_{EBO}	emitter-base cut-off current	I_C = 0; V_{EB} = 6 V	–	50	nA
h_{FE}	DC current gain	V_{CE} = 1 V; (see Fig.2)			
		I_C = 0.1 mA	20	–	
		I_C = 1 mA	40	–	
		I_C = 10 mA	80	–	
		I_C = 150 mA; note 1	100	300	
		I_C = 500 mA; V_{CE} = 2 V; note 1	40	–	
V_{CEsat}	collector-emitter saturation voltage	I_C = 150 mA; I_B = 15 mA; note 1	–	400	mV
		I_C = 500 mA; I_B = 50 mA; note 1	–	750	mV
V_{BEsat}	base-emitter saturation voltage	I_C = 150 mA; I_B = 15 mA; note 1	–	950	mV
		I_C = 500 mA; I_B = 50 mA; note 1	–	1.2	V
C_c	collector capacitance	$I_E = I_e$ = 0; V_{CB} = 5 V; f = 1 MHz	–	8	pF
C_e	emitter capacitance	$I_C = I_c$ = 0; V_{EB} = 500 mV; f = 1 MHz	–	30	pF
f_T	transition frequency	I_C = 20 mA; V_{CE} = 10 V; f = 100 MHz	250	–	MHz
Switching times (between 10% and 90% levels); (see Fig.3)					
t_{on}	turn-on time	I_{Con} = 150 mA; I_{Bon} = 15 mA; I_{Boff} = –15 mA	–	35	ns
t_d	delay time		–	15	ns
t_r	rise time		–	20	ns
t_{off}	turn-off time		–	250	ns
t_s	storage time		–	200	ns
t_f	fall time		–	60	ns

Note

1. Pulse test: $t_p \leq 300\ \mu s$; $\delta \leq 0.02$.

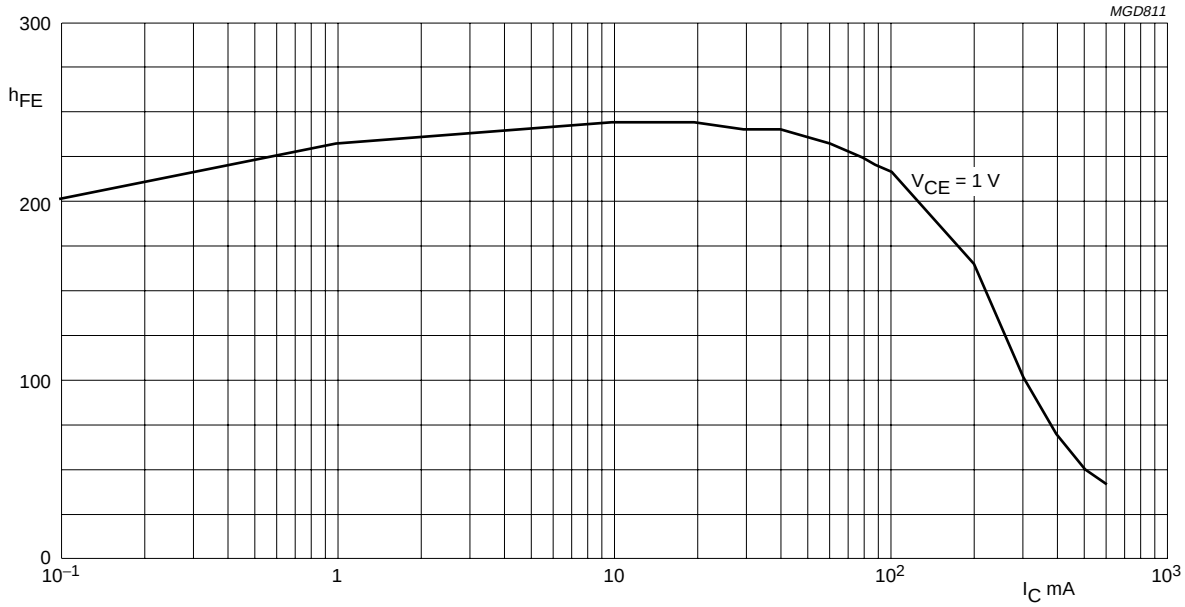
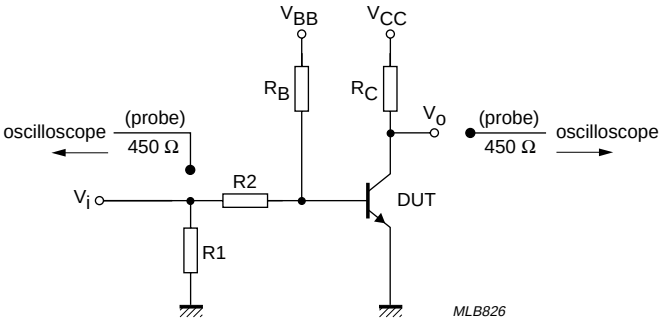


Fig.2 DC current gain; typical values.

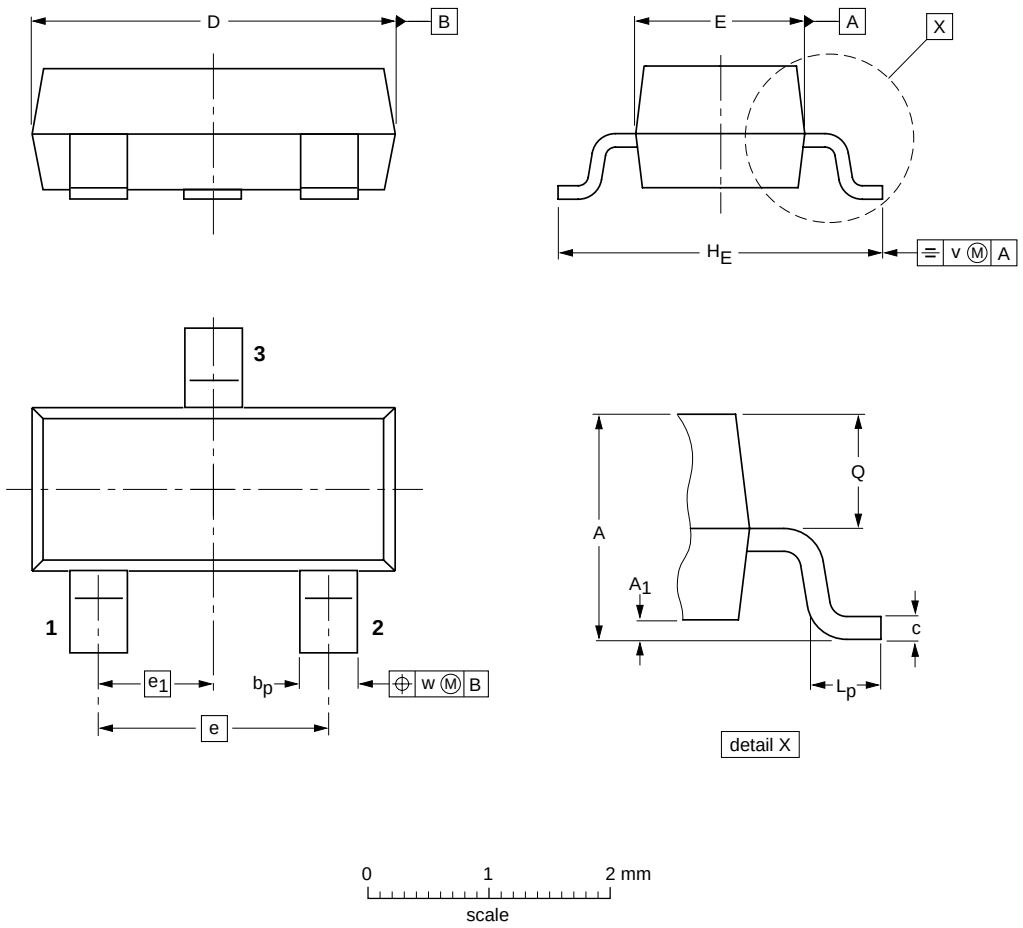


V_i = 9.5 V; T = 500 µs; t_p = 10 µs; t_r = t_f ≤ 3 ns.
R1 = 68 Ω; R2 = 325 Ω; R_B = 325 Ω; R_C = 160 Ω.
V_{BB} = −3.5 V; V_{CC} = 29.5 V.
Oscilloscope: input impedance Z_i = 50 Ω.

Fig.3 Test circuit for switching times.

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads **SOT23**



DIMENSIONS (mm are the original dimensions)

UNIT	A	A_1 max.	b_p	c	D	E	e	e_1	H_E	L_p	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT23		TO-236AB				~~97-02-28~~ 99-09-13

DATA SHEET STATUS

LEVEL	DATA SHEET STATUS(1)	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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