ALPHA & OMEGA SEMICONDUCTOR

RoHS Compliant

AO3401A
P-Channel Enhancement Mode Field Effect Transistor

General Description

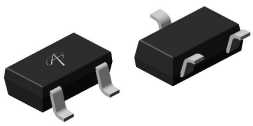
The AO3401A/L uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation gate voltages as low as 2.5V. This device is suitable for use as a load switch or other general applications. AO3401A and AO3401AL are electrically identical.
-RoHS Compliant
-AO3401AL is Halogen Free

Features

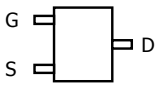
V_{DS} (V) = -30V
I_D = -4.3A (V_{GS} = -10V)
$R_{DS(ON)}$ < 46mΩ (V_{GS} = -10V)
$R_{DS(ON)}$ < 55mΩ (V_{GS} = -4.5V)
$R_{DS(ON)}$ < 80mΩ (V_{GS} = -2.5V)

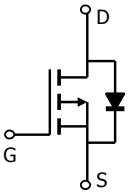
Rg,Ciss,Coss,Crss Tested

TO-236 (SOT-23)



Top View





Absolute Maximum Ratings T_A=25°C unless otherwise noted

Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	±12	V
Continuous Drain Current [A,F]	T_A=25°C	I_D	-4.3	A
	T_A=70°C		-3.8	
Pulsed Drain Current [B]		I_{DM}	-25	
Power Dissipation [A]	T_A=25°C	P_D	1.4	W
	T_A=70°C		0.9	
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

Thermal Characteristics

Parameter		Symbol	Typ	Max	Units
Maximum Junction-to-Ambient [AF]	t ≤ 10s	$R_{\theta JA}$	65	90	°C/W
Maximum Junction-to-Ambient [A]	Steady-State		85	125	°C/W
Maximum Junction-to-Lead [C]	Steady-State	$R_{\theta JL}$	43	80	°C/W

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	I_D=-250µA, V_{GS}=0V	-30			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=-30V, V_{GS}=0V			-1	µA
		T_J=55°C			-5	
I_{GSS}	Gate-Body leakage current	V_{DS}=0V, V_{GS}=±12V			±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS} I_D=-250µA	-0.6	-1	-1.3	V
$I_{D(ON)}$	On state drain current	V_{GS}=-4.5V, V_{DS}=-5V	-25			A
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=-10V, I_D=-4.3A		36	44	mΩ
		T_J=125°C		52	63	
		V_{GS}=-4.5V, I_D=-3.5A		44	55	mΩ
		V_{GS}=-2.5V, I_D=-2.5A		62	80	mΩ
g_{FS}	Forward Transconductance	V_{DS}=-5V, I_D=-4.3A		13		S
V_{SD}	Diode Forward Voltage	I_S=-1A,V_{GS}=0V		-0.75	-1	V
I_S	Maximum Body-Diode Continuous Current				-2	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=-15V, f=1MHz		933	1200	pF
C_{oss}	Output Capacitance			108		pF
C_{rss}	Reverse Transfer Capacitance			81		pF
R_g	Gate resistance	V_{GS}=0V, V_{DS}=0V, f=1MHz		6	9	Ω
SWITCHING PARAMETERS						
Q_g	Total Gate Charge	V_{GS}=-4.5V, V_{DS}=-15V, I_D=-4.3A		9.3	12.2	nC
Q_{gs}	Gate Source Charge			1.5		nC
Q_{gd}	Gate Drain Charge			3.7		nC
$t_{D(on)}$	Turn-On DelayTime	V_{GS}=-10V, V_{DS}=-15V, R_L=3.5Ω, R_{GEN}=6Ω		5.2		ns
t_r	Turn-On Rise Time			6.8		ns
$t_{D(off)}$	Turn-Off DelayTime			42		ns
t_f	Turn-Off Fall Time			15		ns
t_{rr}	Body Diode Reverse Recovery Time	I_F=-4.3A, dI/dt=100A/µs		21	28	ns
Q_{rr}	Body Diode Reverse Recovery Charge	I_F=-4.3A, dI/dt=100A/µs		14.3		nC

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in ^{2} FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The value in any given application depends on the user's specific board design.

B: Repetitive rating, pulse width limited by junction temperature.

C. The $R_{\theta JA}$ is the sum of the thermal impedence from junction to lead $R_{\theta JL}$ and lead to ambient.

D. The static characteristics in Figures 1 to 6 are obtained using <300 µs pulses, duty cycle 0.5% max.

E. These tests are performed with the device mounted on 1 in ^{2} FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The SOA curve provides a single pulse rating.

F.The current rating is based on the t≤ 10s thermal resistance rating.

Rev2: Aug.2008

THIS PRODUCT HAS BEEN DESIGNED AND QUALIFIED FOR THE CONSUMER MARKET. APPLICATIONS OR USES AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS ARE NOT AUTHORIZED. AOS DOES NOT ASSUME ANY LIABILITY ARISING OUT OF SUCH APPLICATIONS OR USES OF ITS PRODUCTS. AOS RESERVES THE RIGHT TO IMPROVE PRODUCT DESIGN, FUNCTIONS AND RELIABILITY WITHOUT NOTICE.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

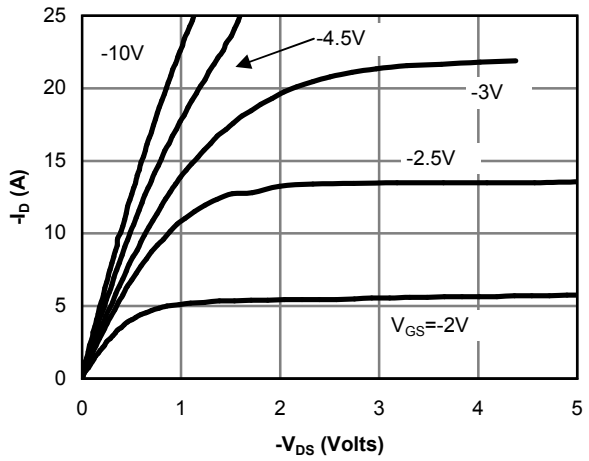


Figure 1: On-Region Characteristics

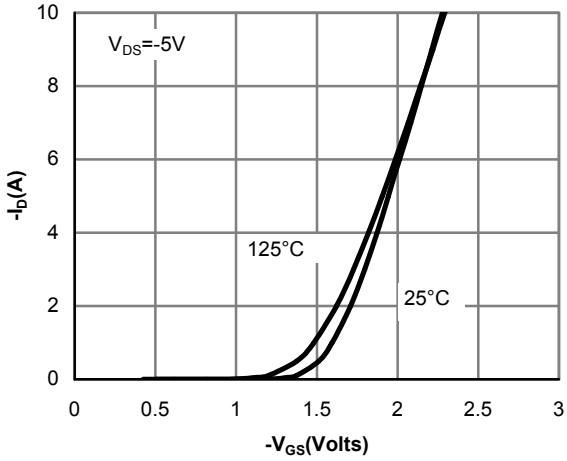


Figure 2: Transfer Characteristics

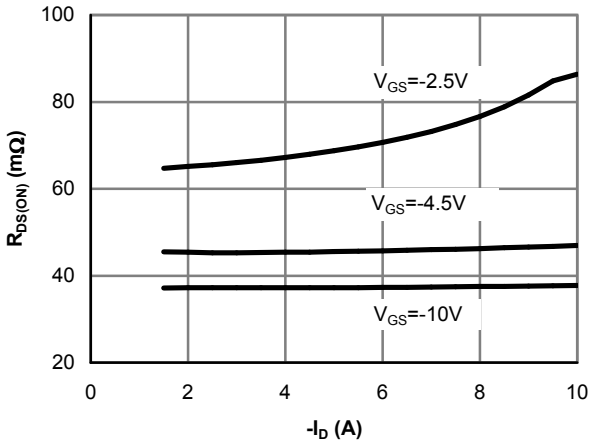


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

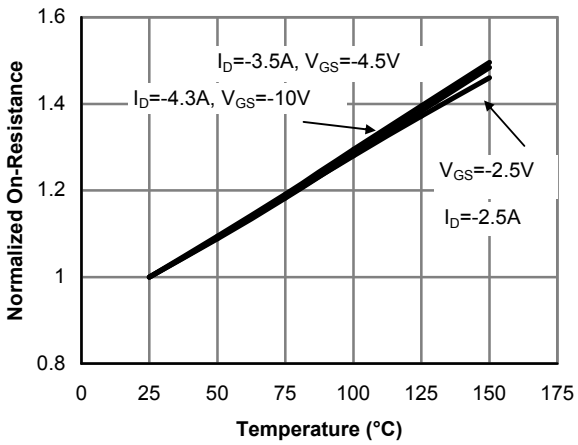


Figure 4: On-Resistance vs. Junction Temperature

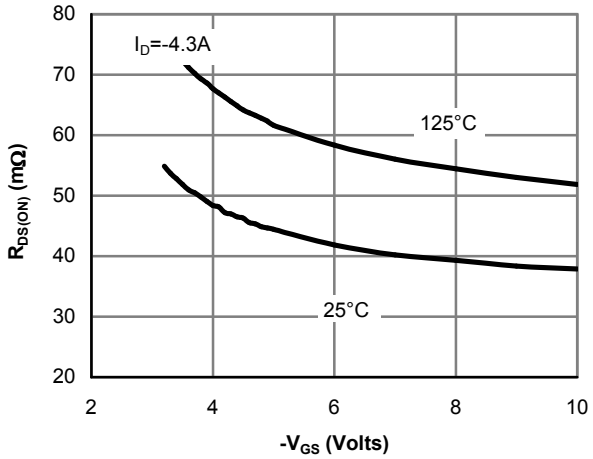


Figure 5: On-Resistance vs. Gate-Source Voltage

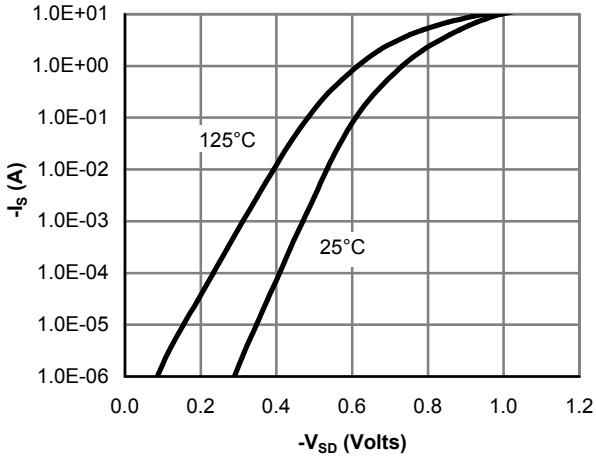


Figure 6: Body-Diode Characteristics

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

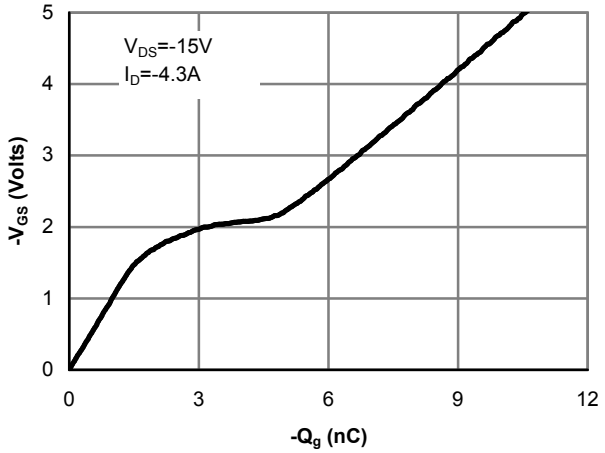


Figure 7: Gate-Charge Characteristics

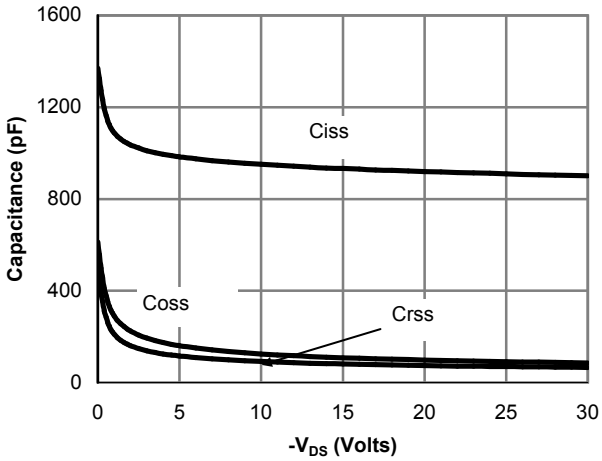


Figure 8: Capacitance Characteristics

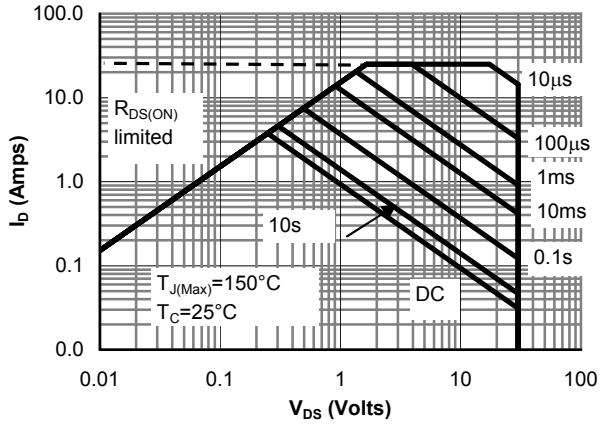


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

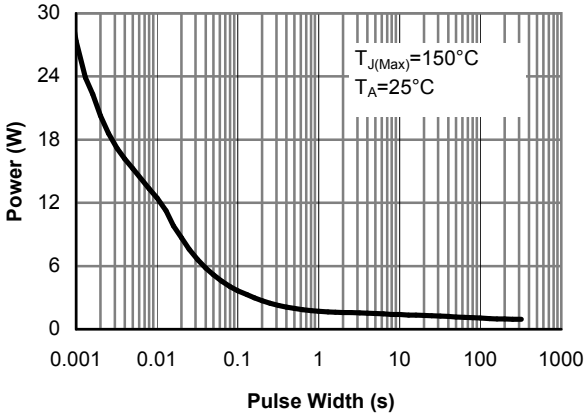


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

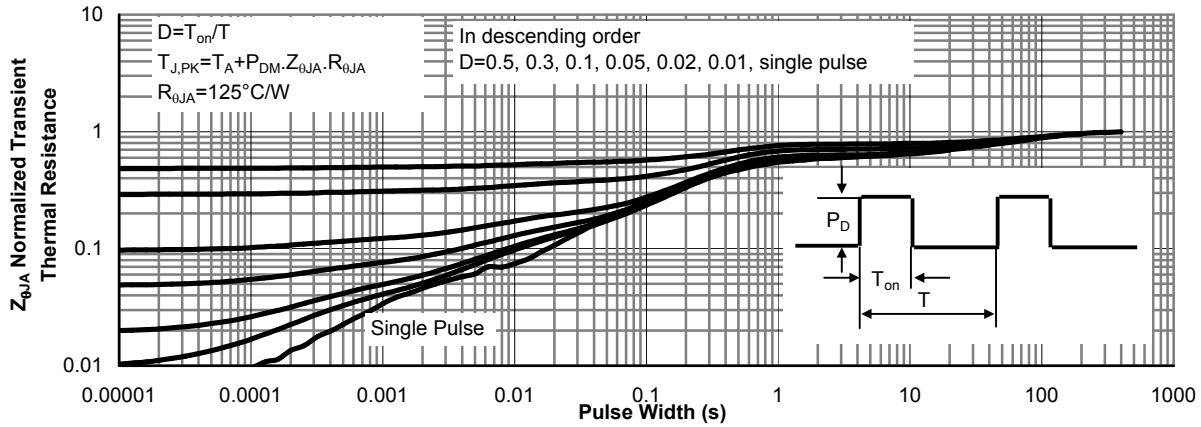
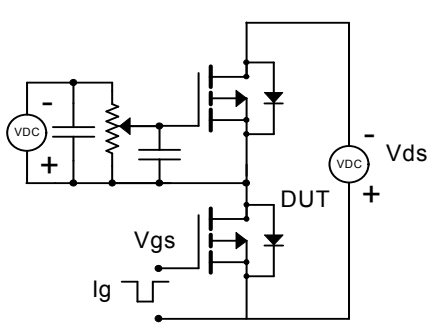
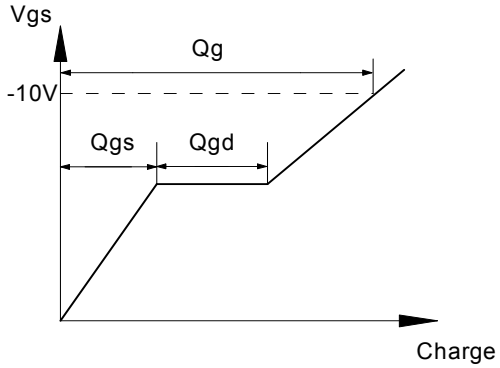


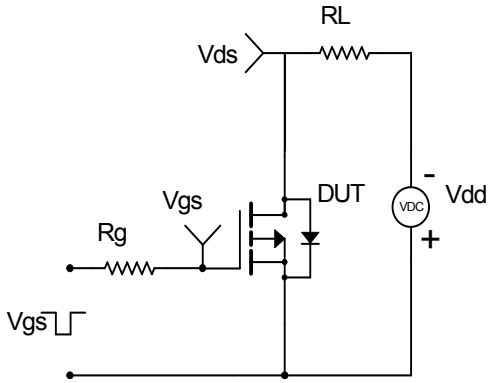
Figure 11: Normalized Maximum Transient Thermal Impedance

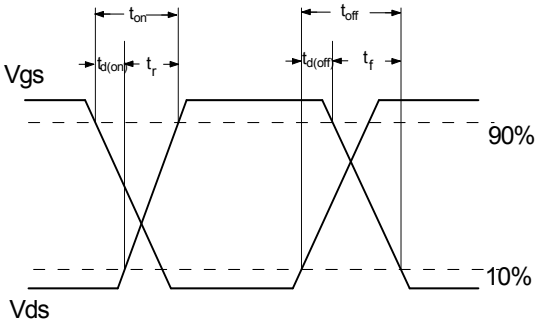
Gate Charge Test Circuit & Waveform



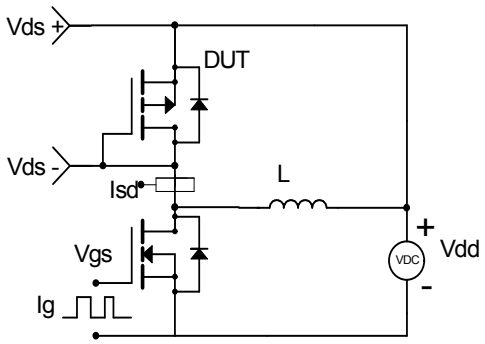


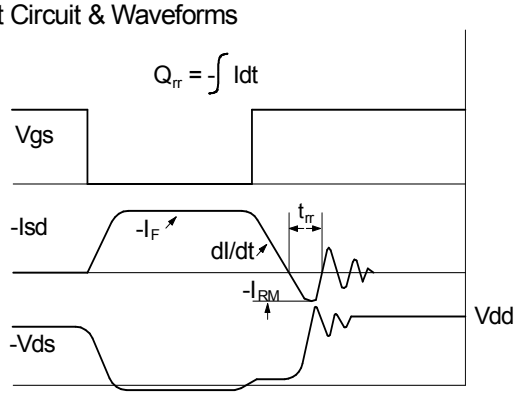
Resistive Switching Test Circuit & Waveforms





Diode Recovery Test Circuit & Waveforms

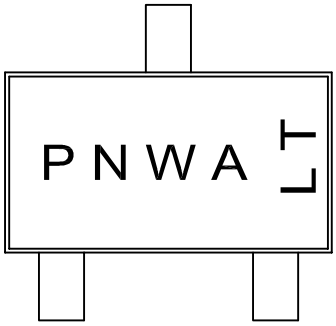




ALPHA & OMEGA SEMICONDUCTOR

Document No.	PD-00660
Version	B
Title	AO3401A Marking Description

SOT-23 PACKAGE MARKING DESCRIPTION



Green product

NOTE:
P - Package and product type
N - Last digital of product number
W - Week code
A - Assembly location code
L&T - Assembly lot code

PART NO.	DESCRIPTION	CODE (PN)
AO3401A	Green product	X1
AO3401AL	Green product	X1